

Figure 01 - Parallel Optical Fiber

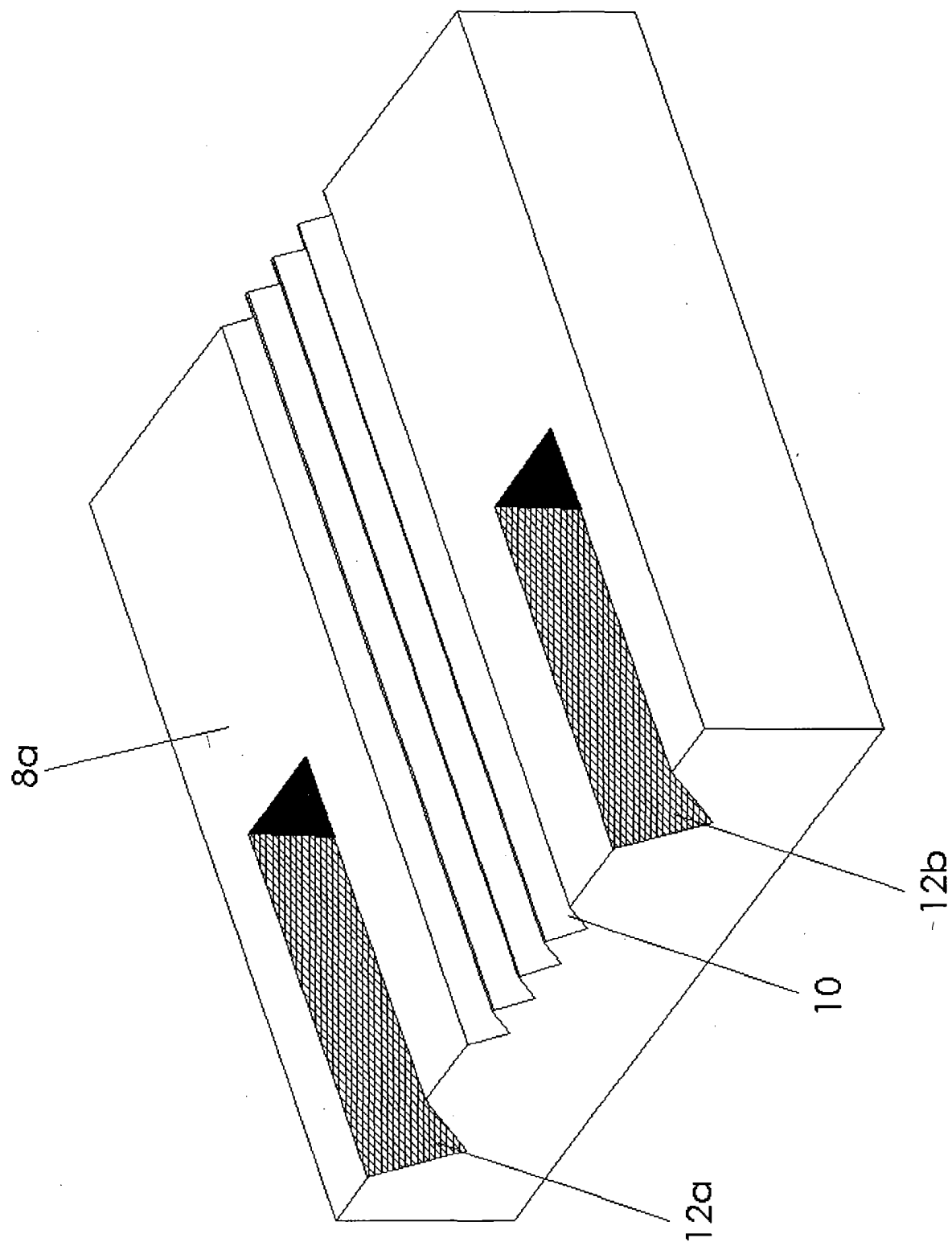


Figure 02a - First Silicon V-Groove Chip (Perspective)

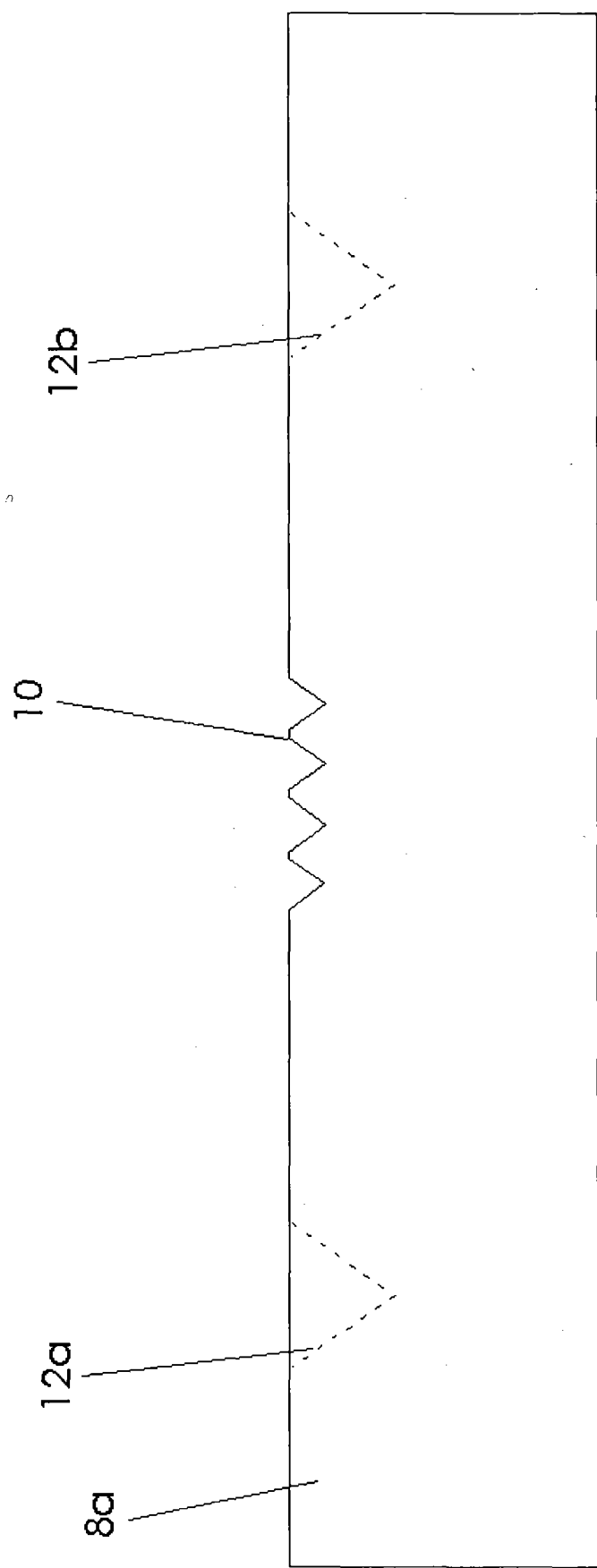


Figure 02b - First Silicon V-Groove Chip (Front)

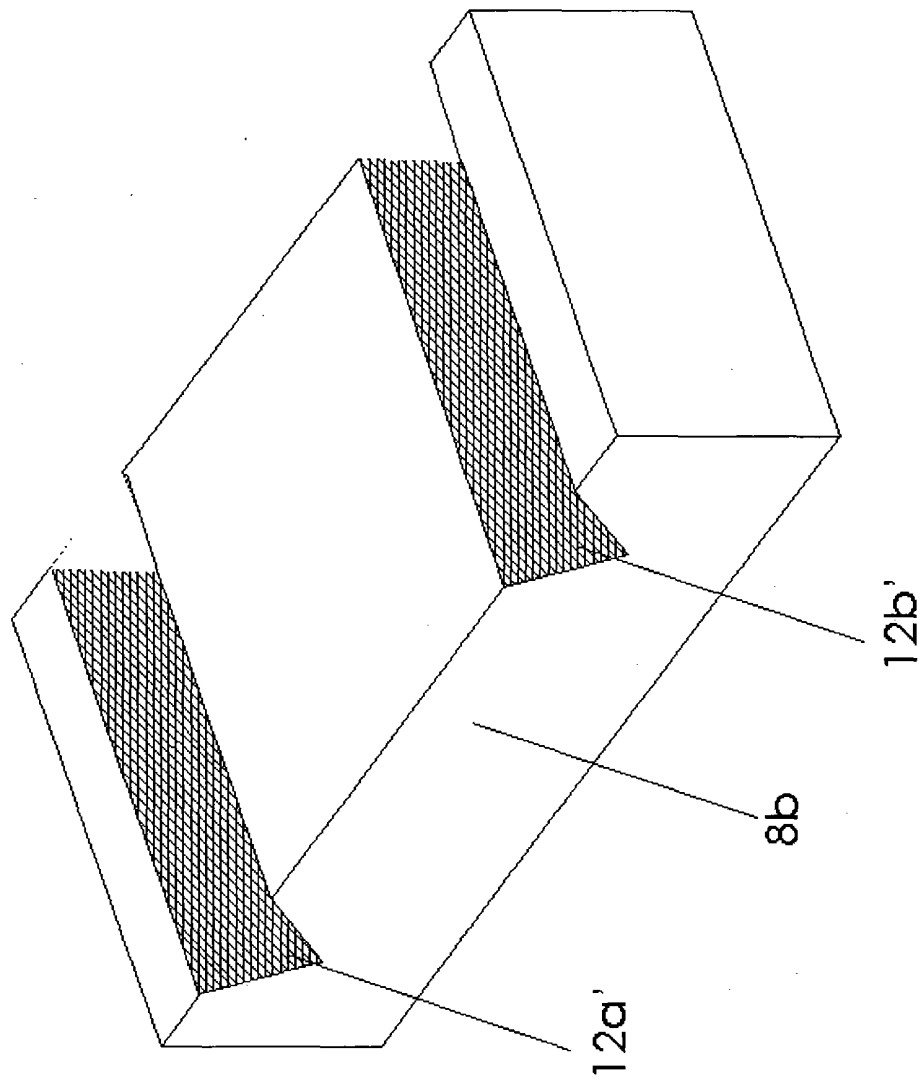


Figure 03a - Second Silicon V-Groove Chip (Perspective)

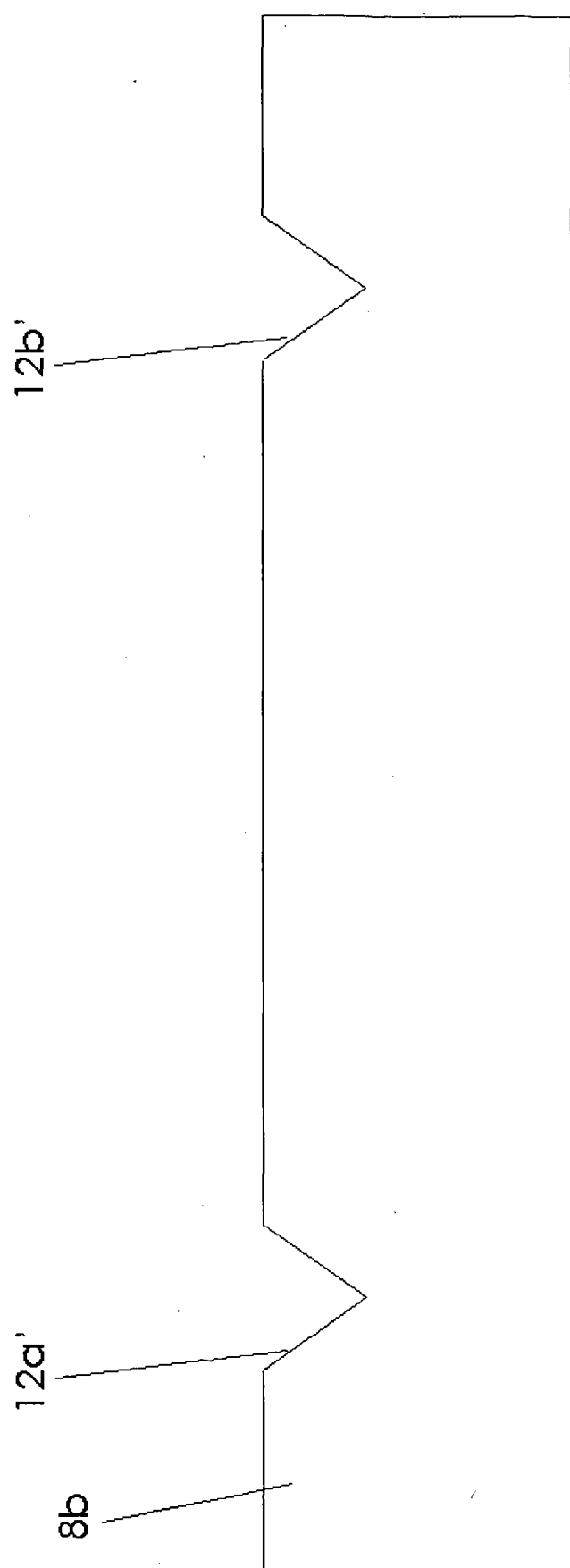


Figure 03b - Second Silicon V-Groove Chip (Front)

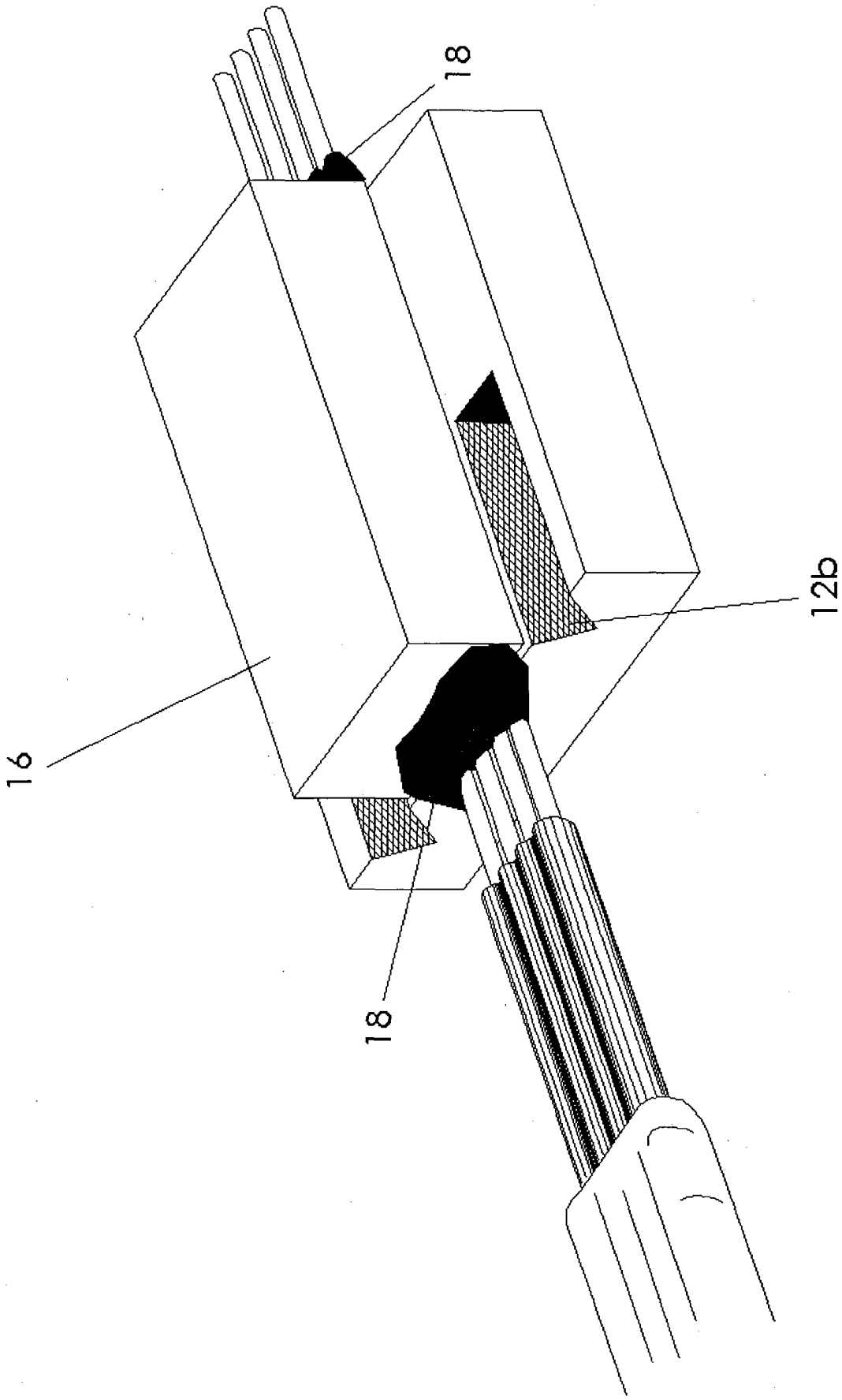


Figure 04a - Cover Plate (glass) (Perspective)

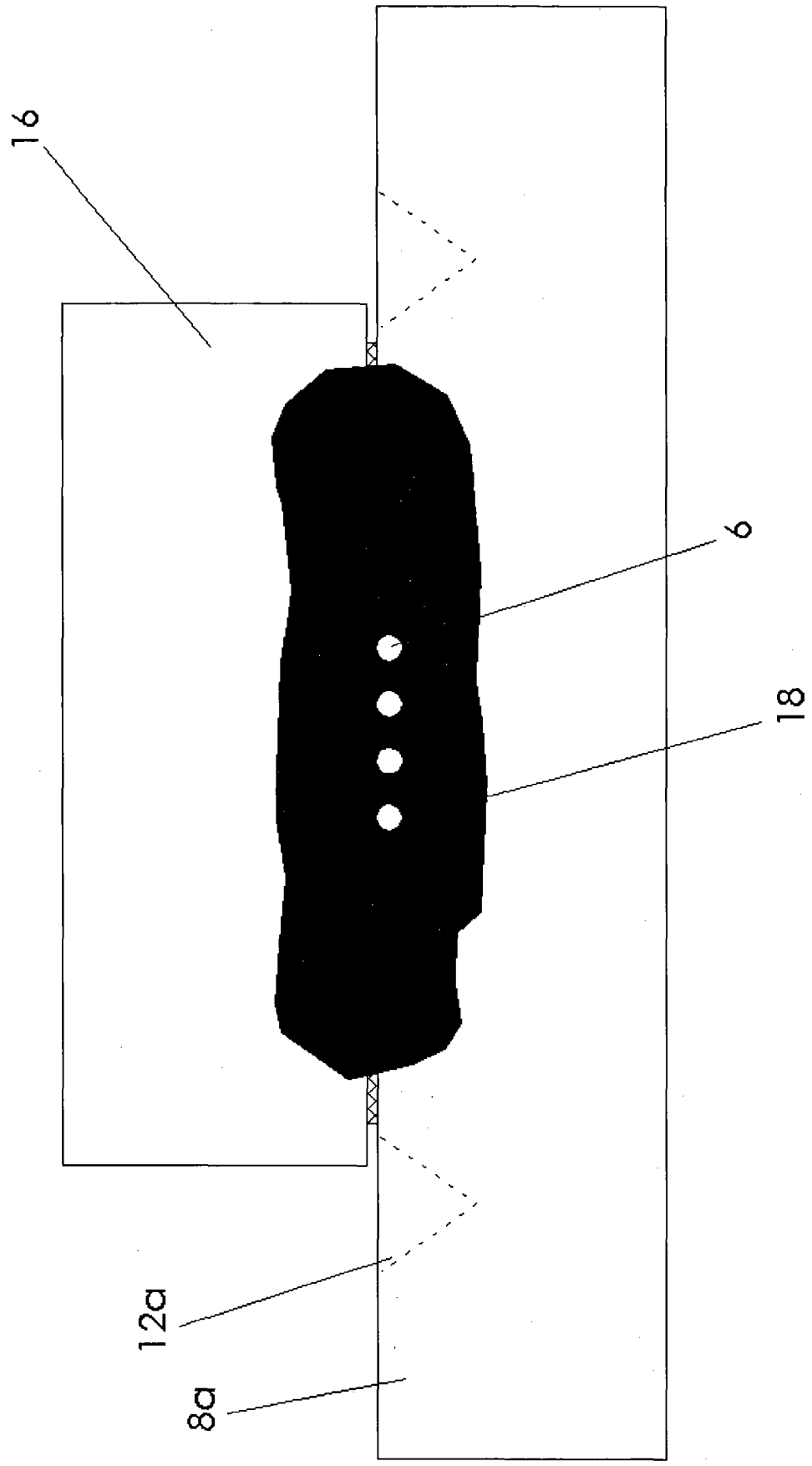


Figure 04b - Cover Plate (glass) (Front)

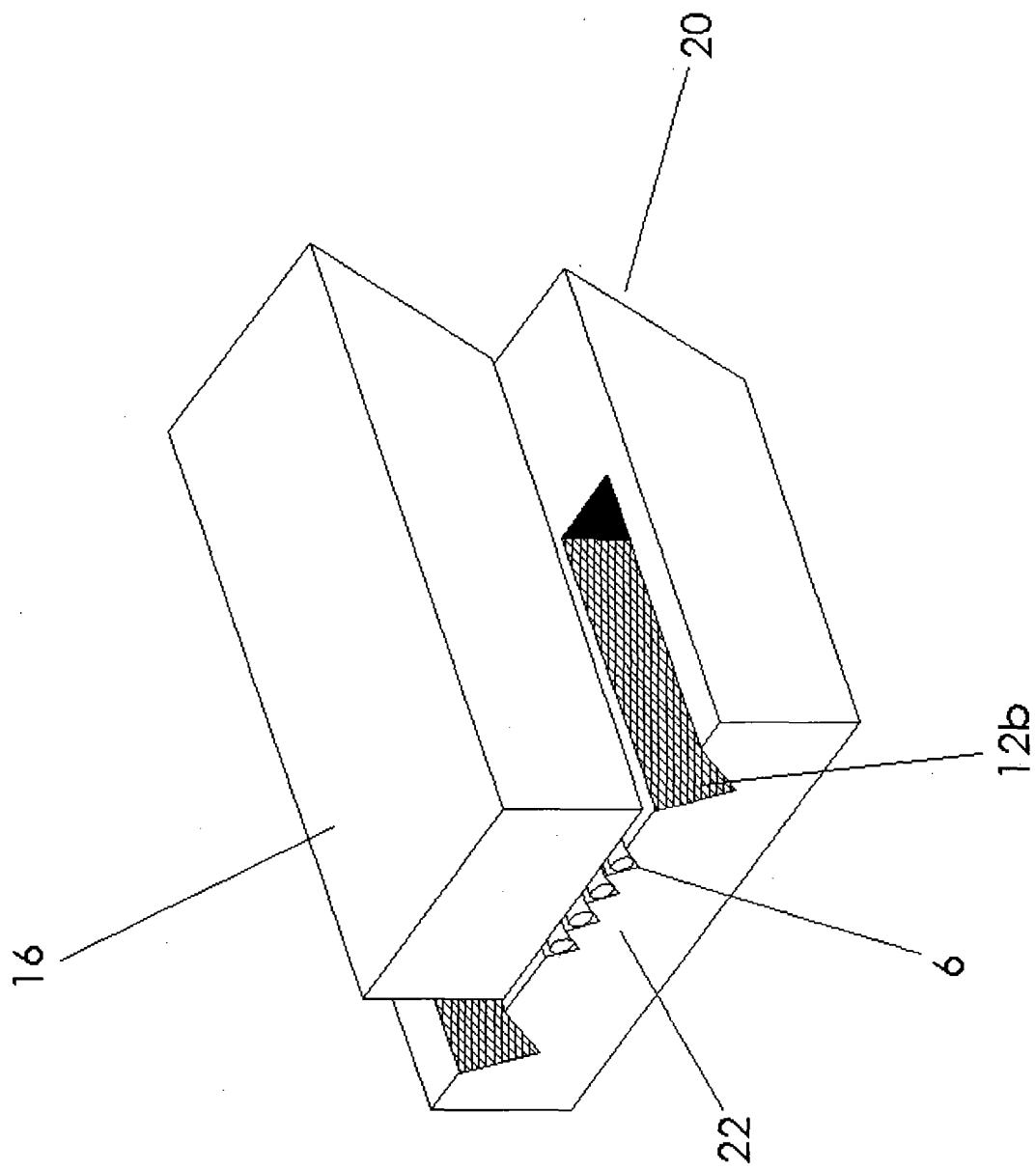


Figure 05a - Bevel Polish and Back Polish (Perspective)



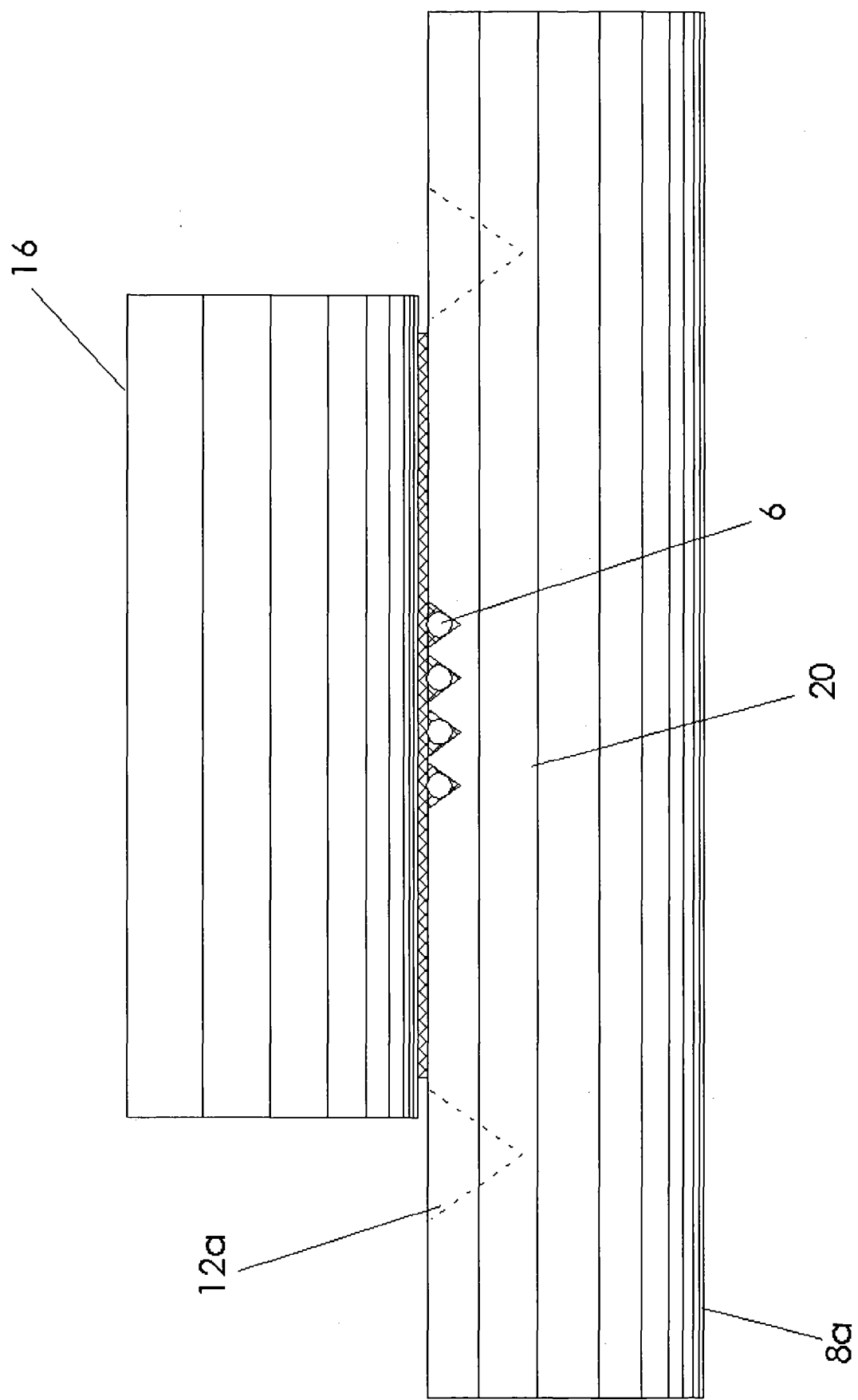


Figure 05b - Bevel Polish and Back Polish (Front)

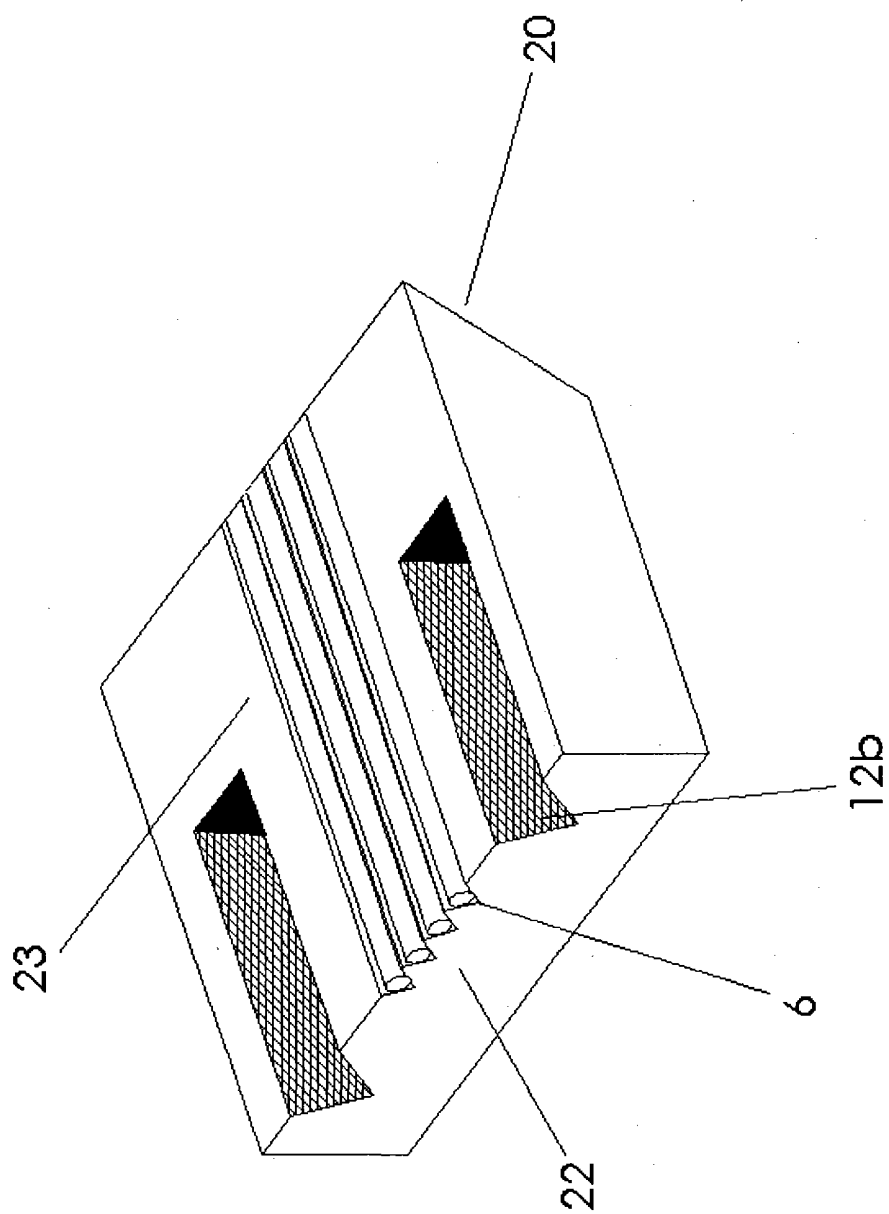


Figure 06a - Removal of Cover Plate (Perspective)

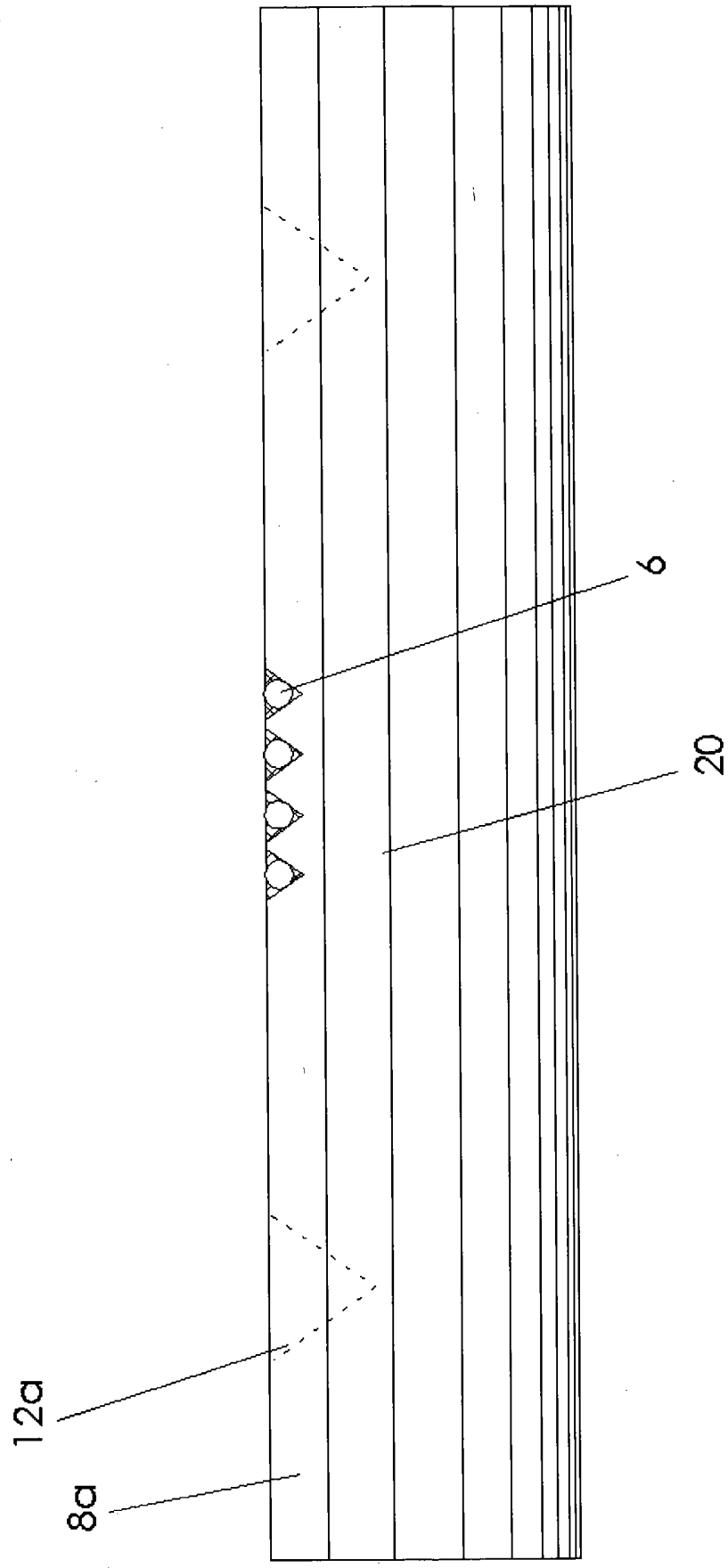


Figure 06b - Removal of Cover Plate (Front)

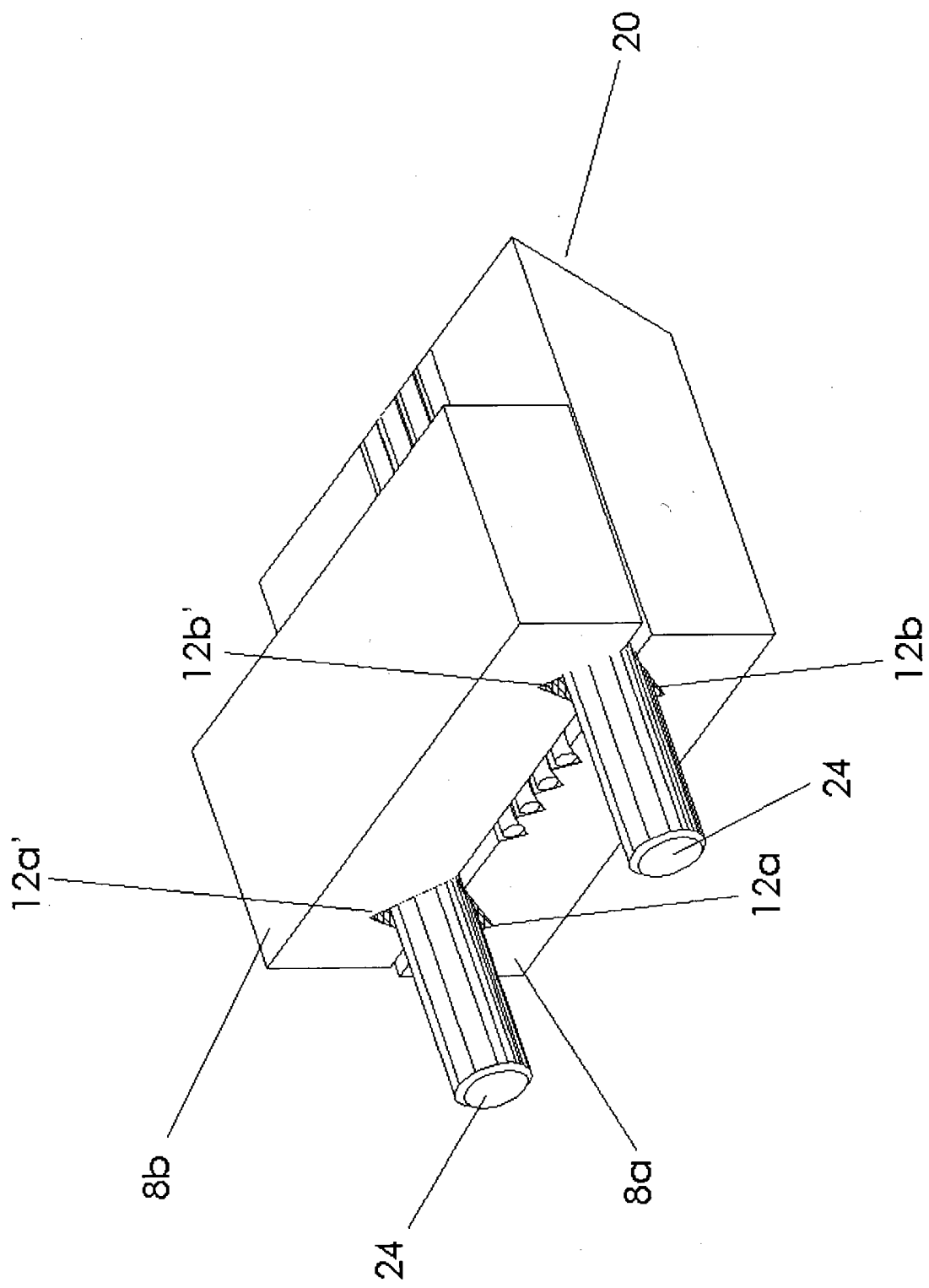


Figure 07a - Complete Ferrule (Perspective)

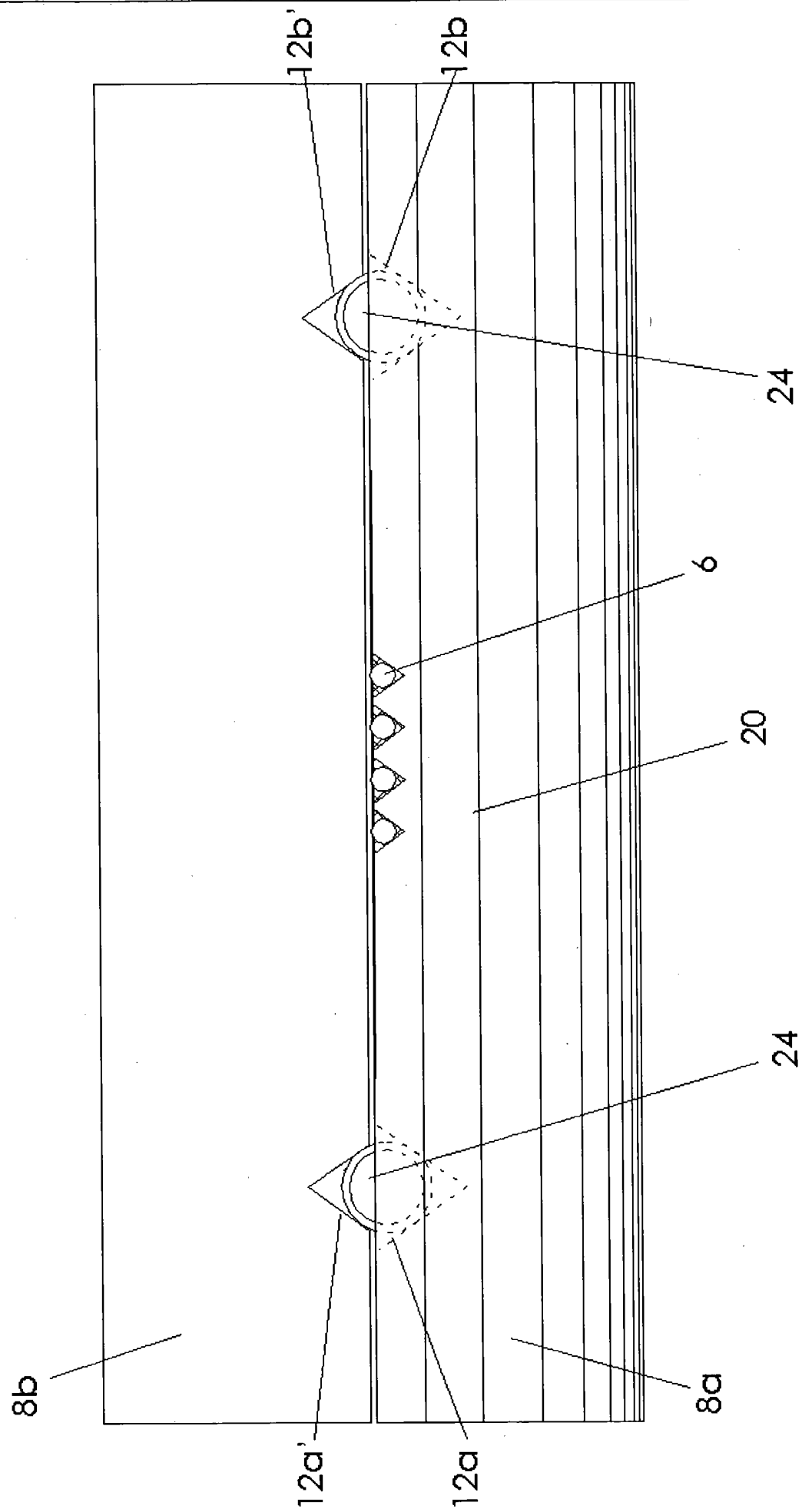


Figure 07b - Complete Ferrule (Front)

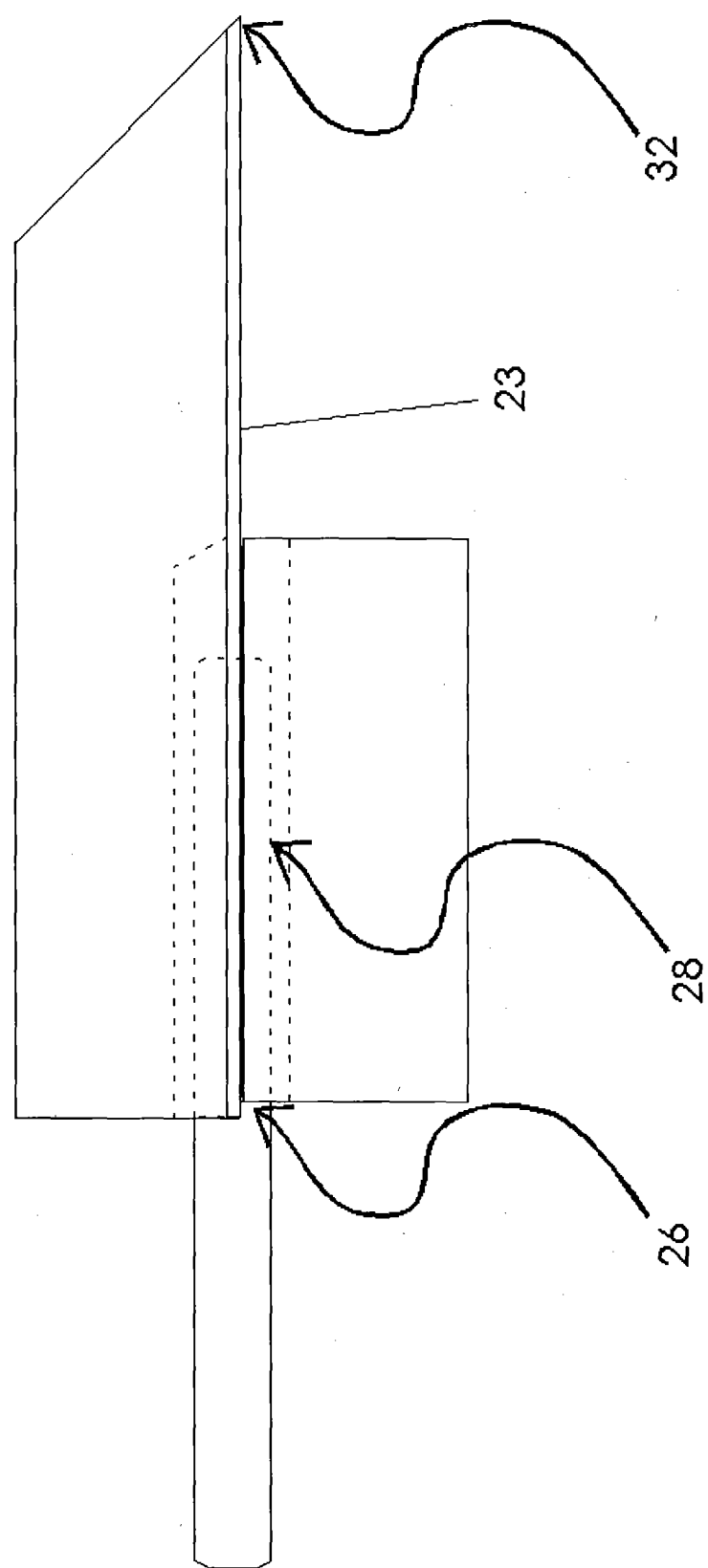


Figure 08a - Complete Ferrule (to Scale Side View)

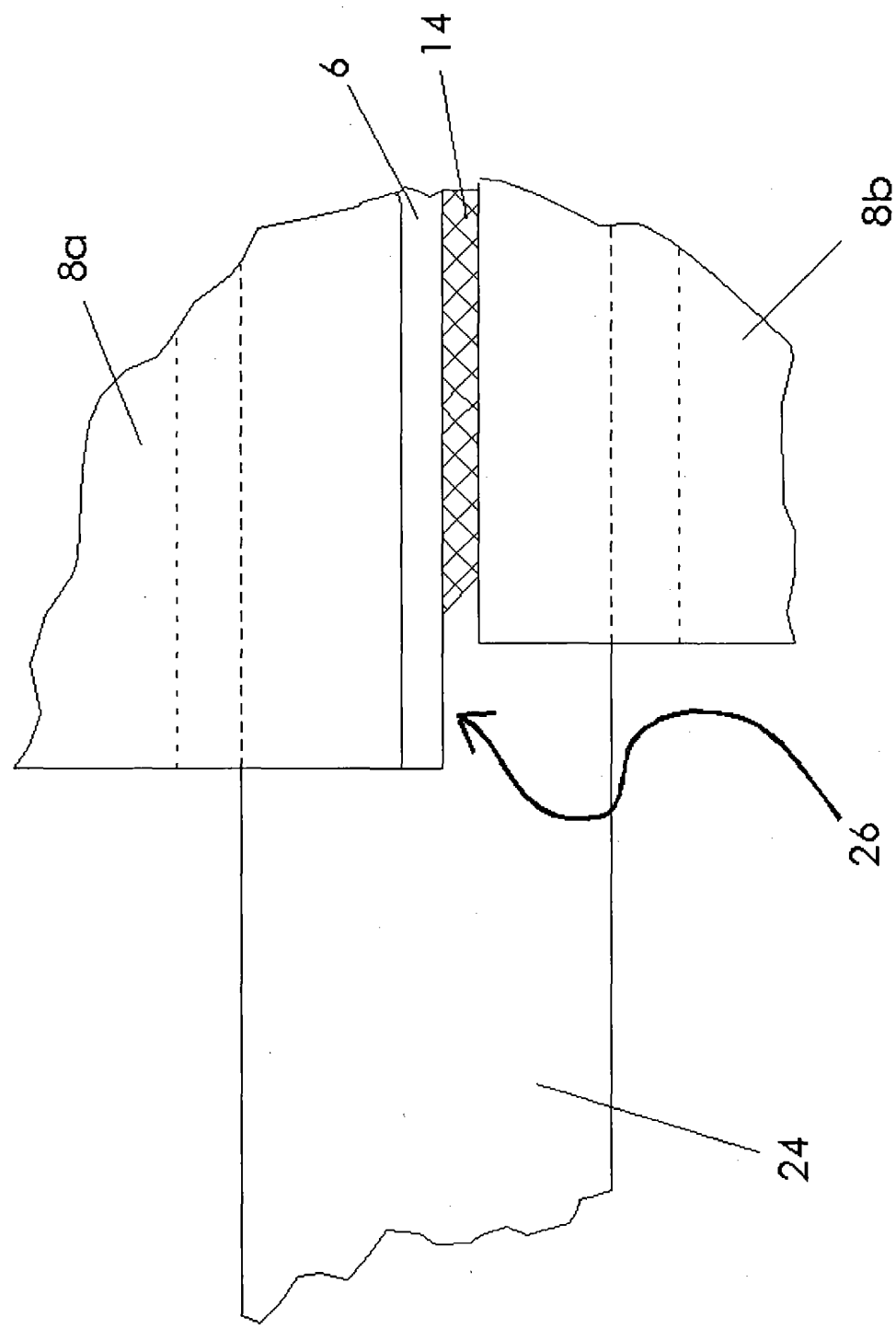


Figure 08b - Stand-off of Second Silicon Chip (Zoom-In)

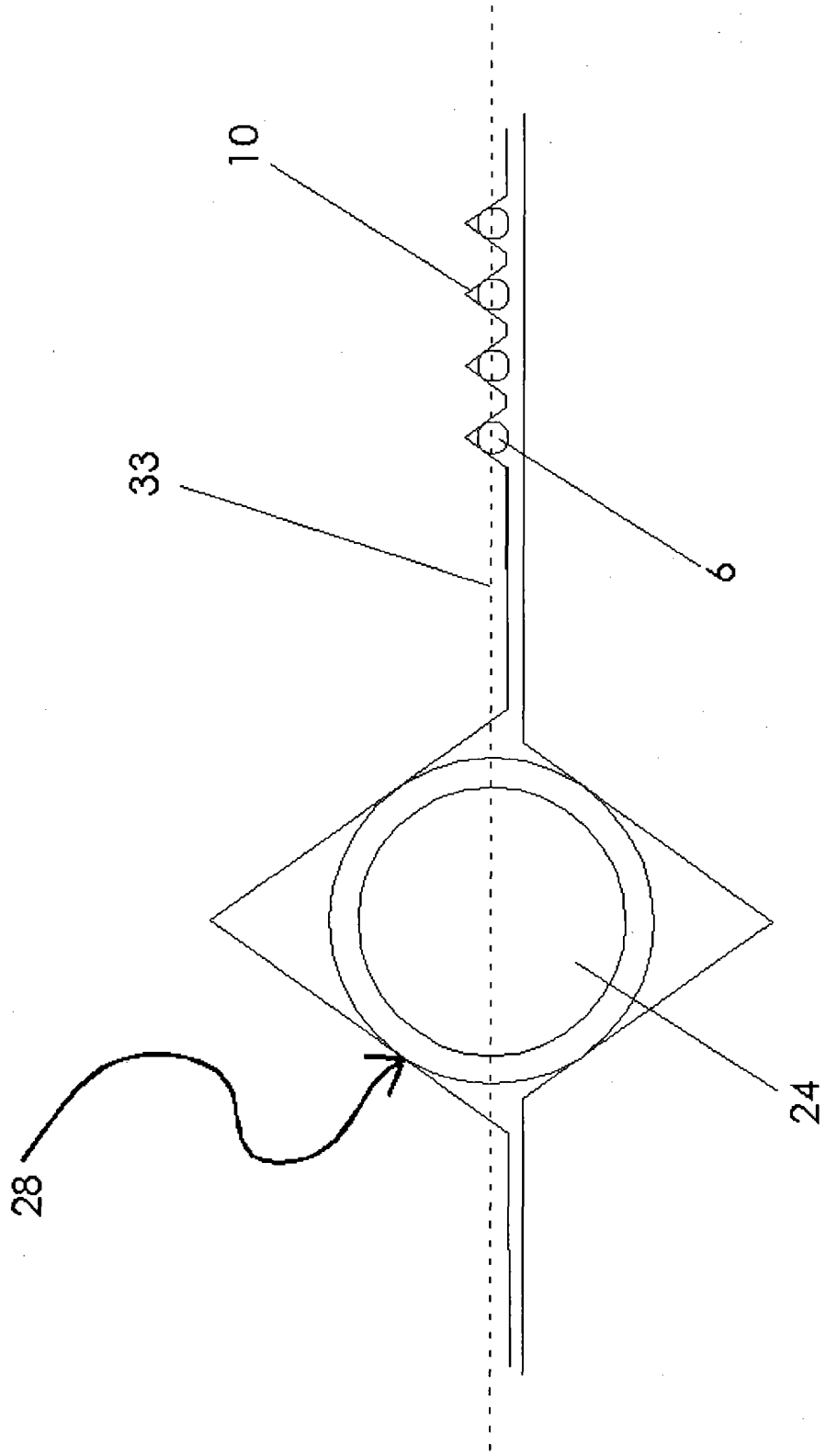


Figure 08c - Geometry of the Dowel Pins (Zoom-In)



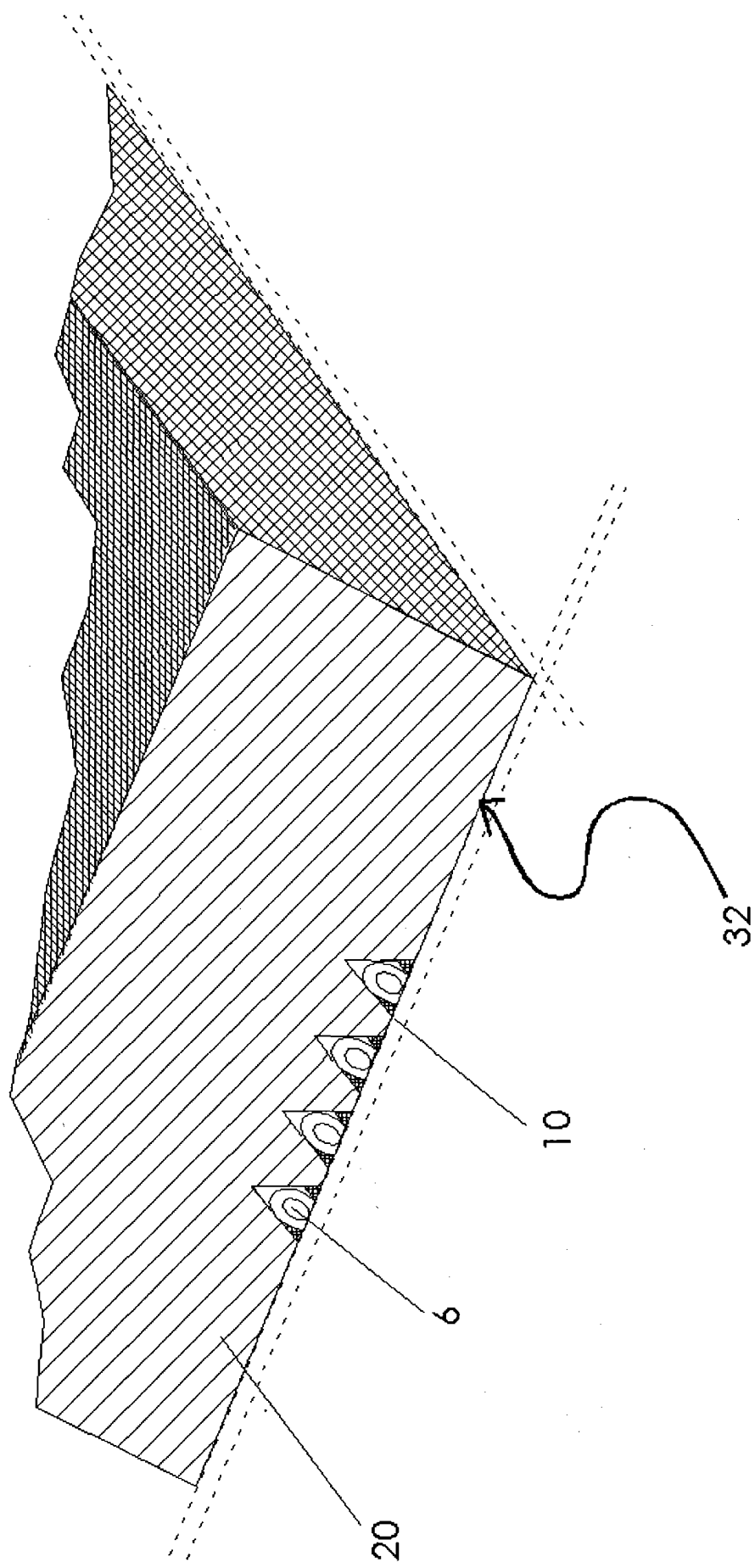


Figure 08d - Over-Polish of the Optical Fibers (Zoom-In Perspective)

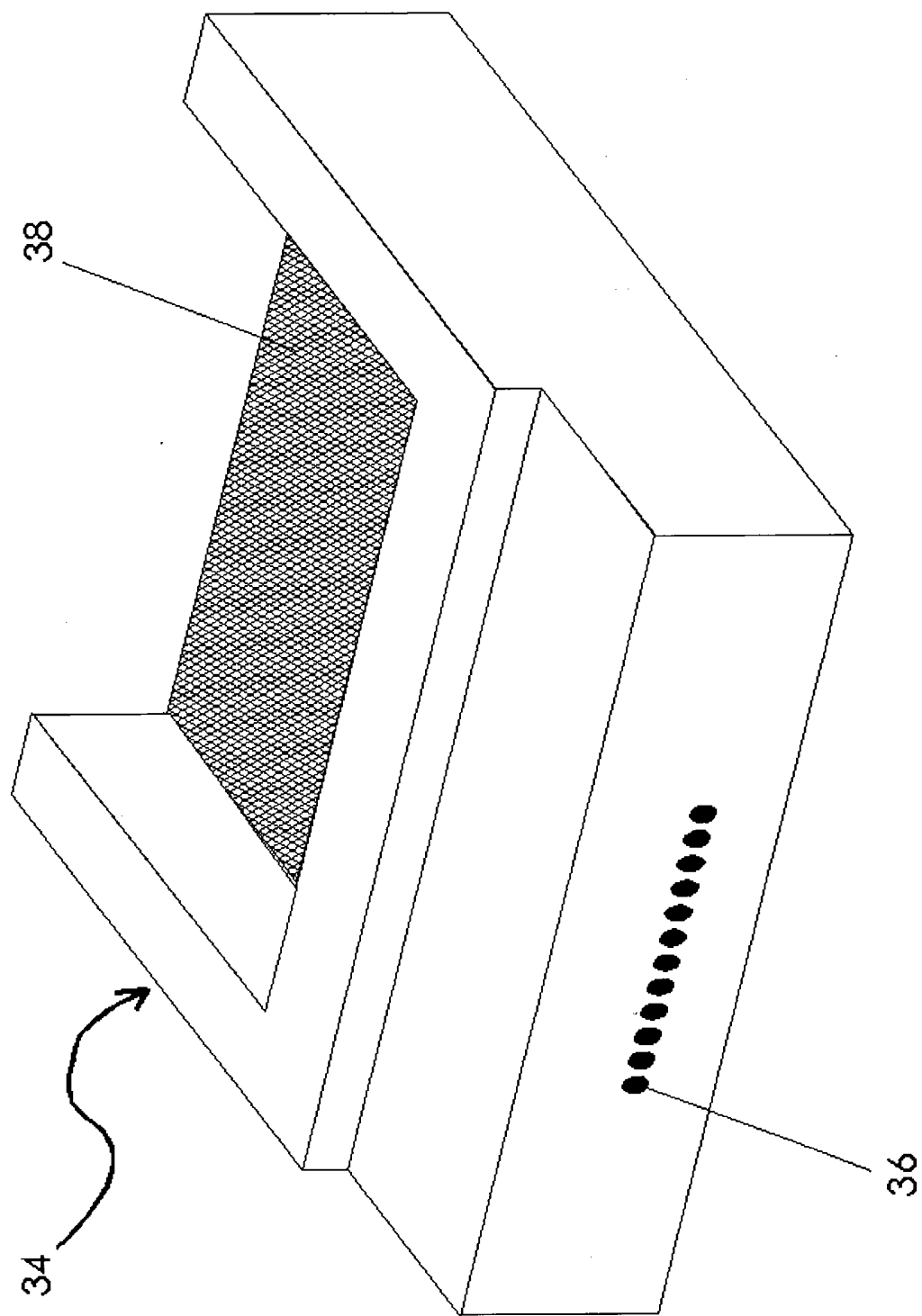


Figure 09a - First Plastic Ferrule (Perspective)

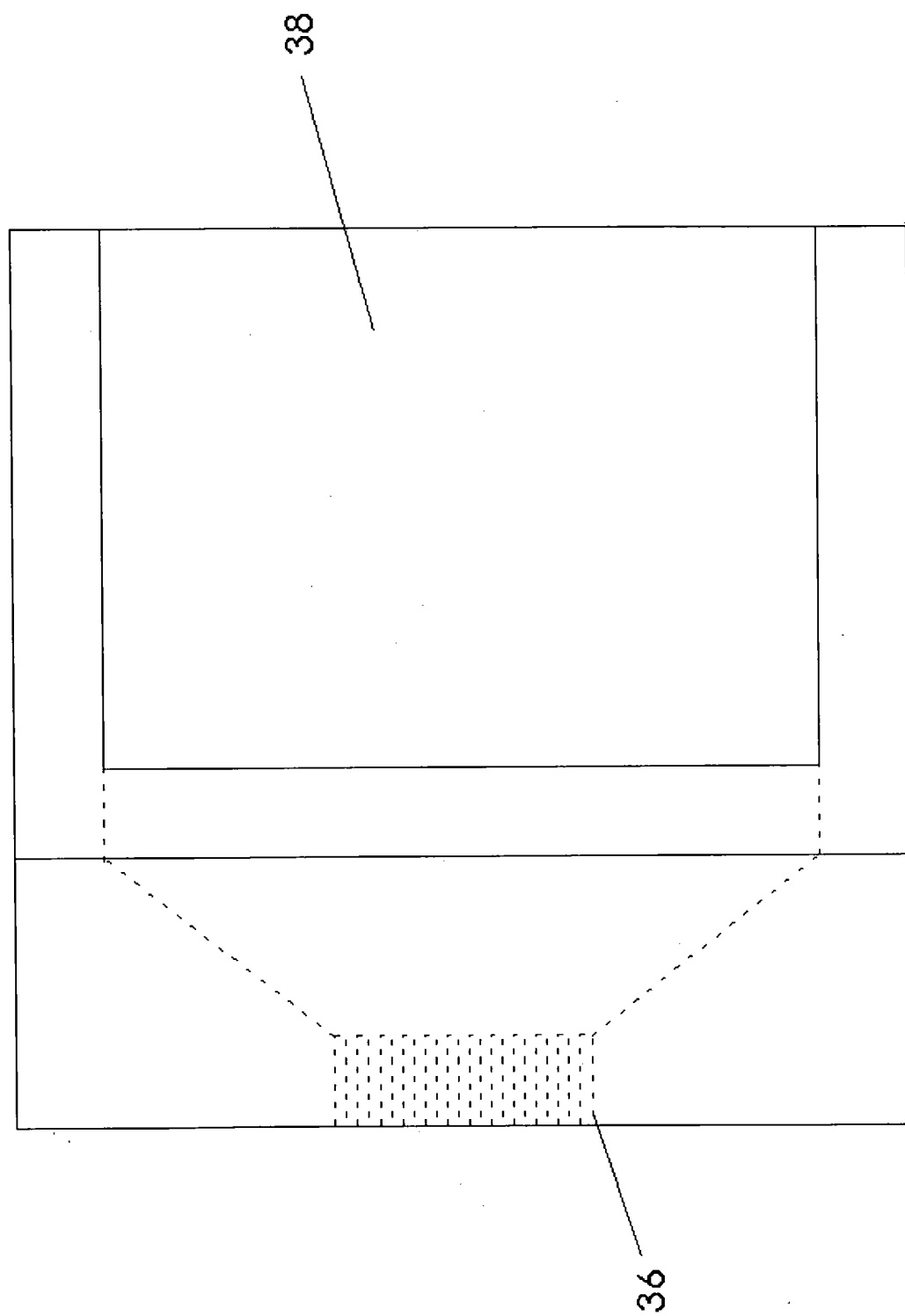


Figure 09b - First Plastic Ferrule (Top)

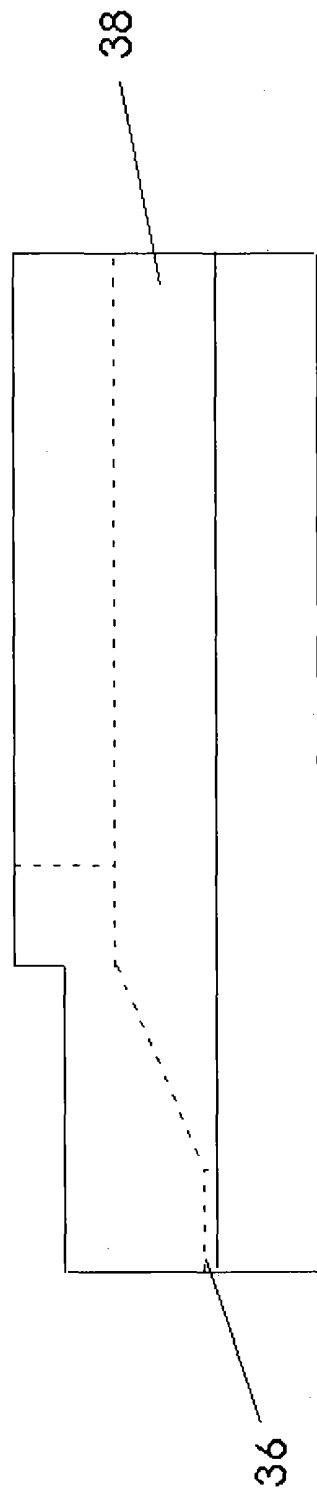


Figure 09c - First Plastic Ferrule (Side)

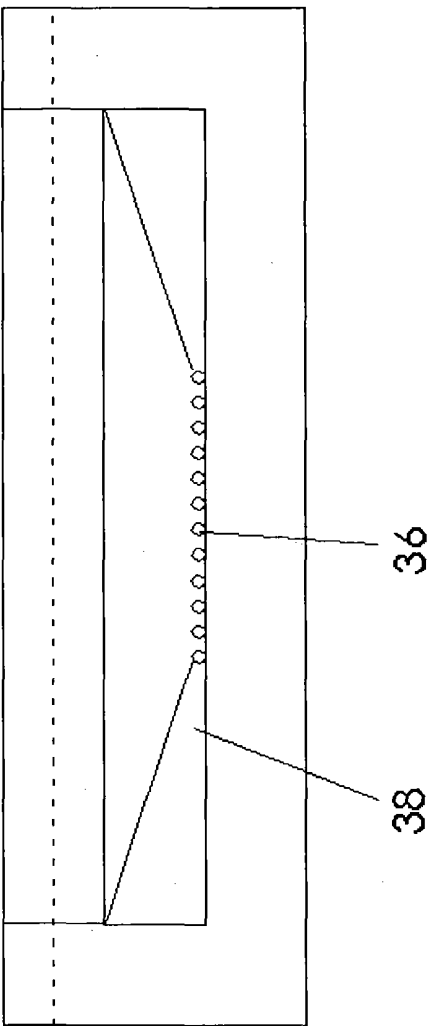


Figure 09d - First Plastic Ferrule (Back)

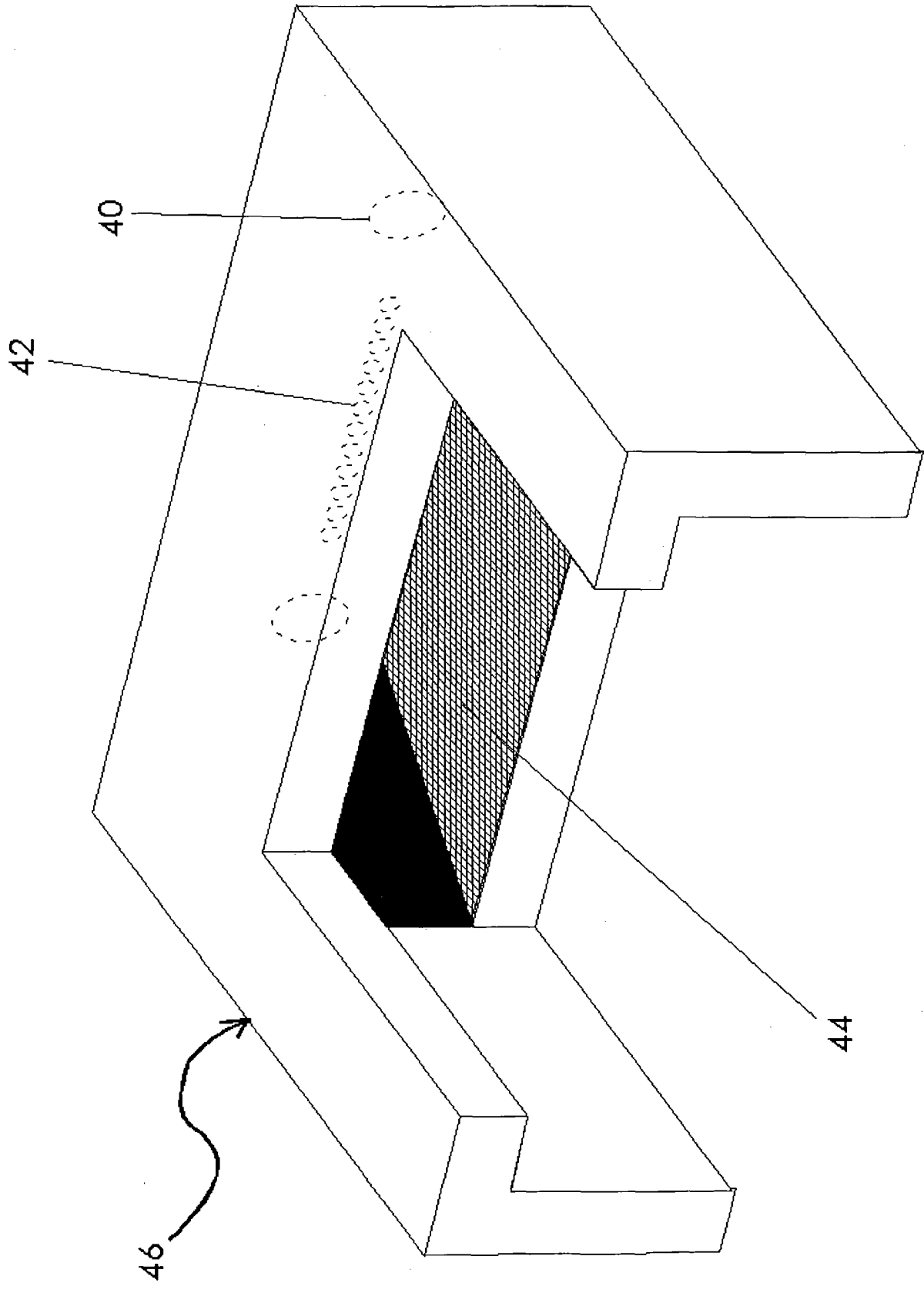


Figure 10a - Second Plastic Ferrule (Perspective)

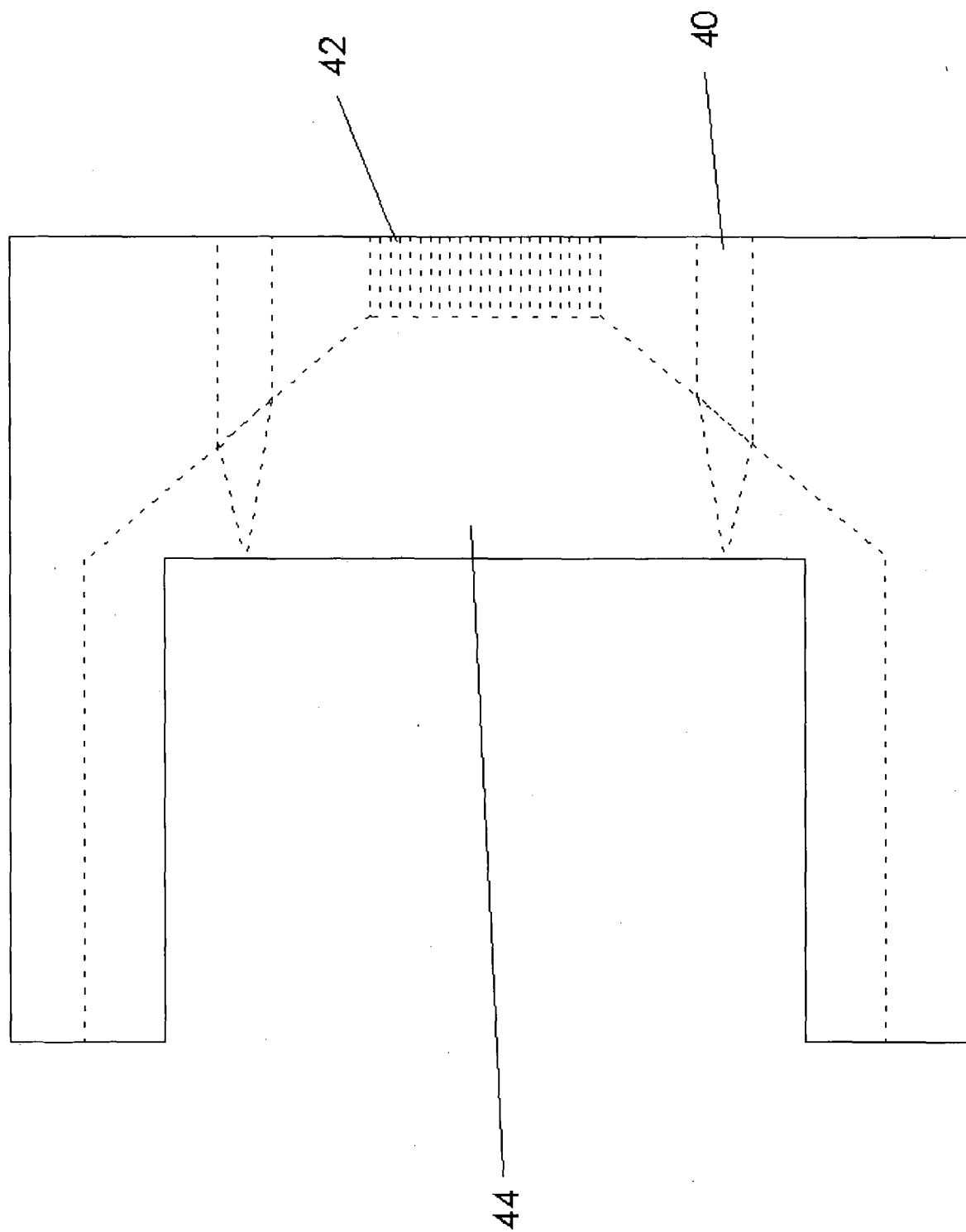


Figure 10b - Second Plastic Ferrule (Top)

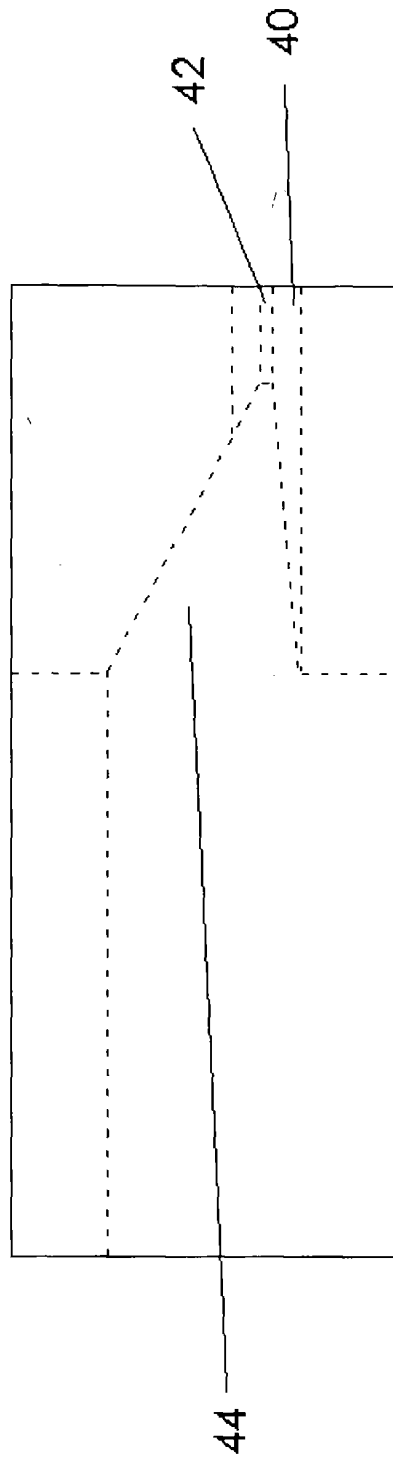


Figure 10c - Second Plastic Ferrule (Side)



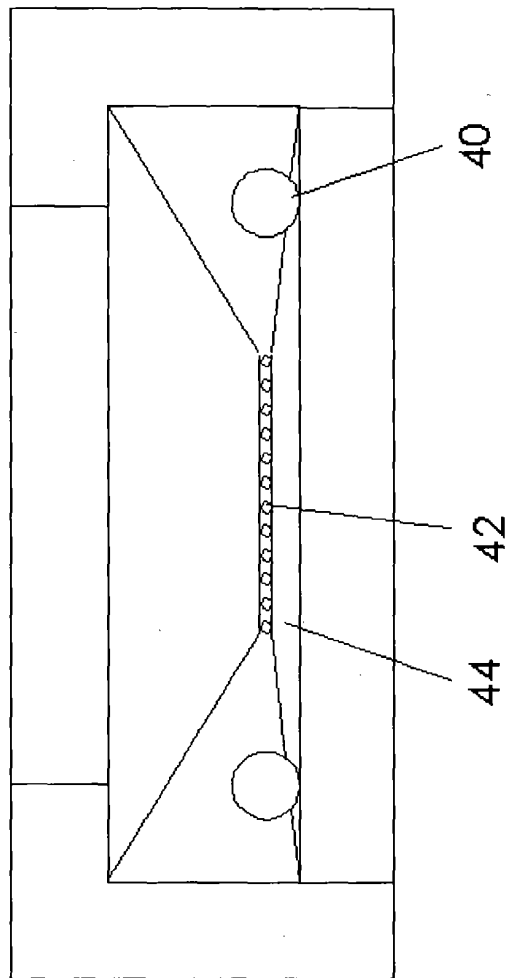


Figure 10d - Second Plastic Ferrule (Back)

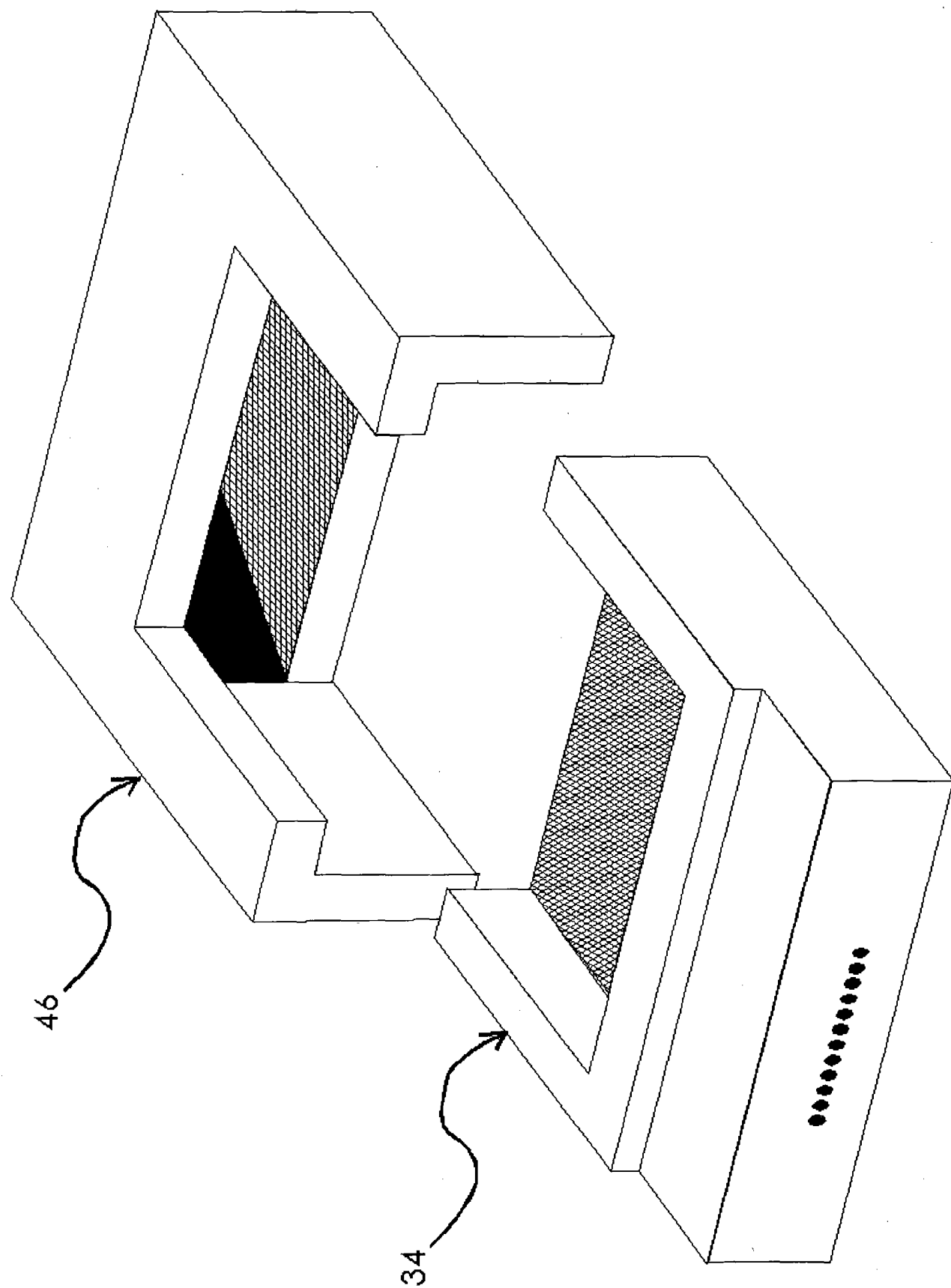


Figure 11 - Both Ferrules (Perspective)

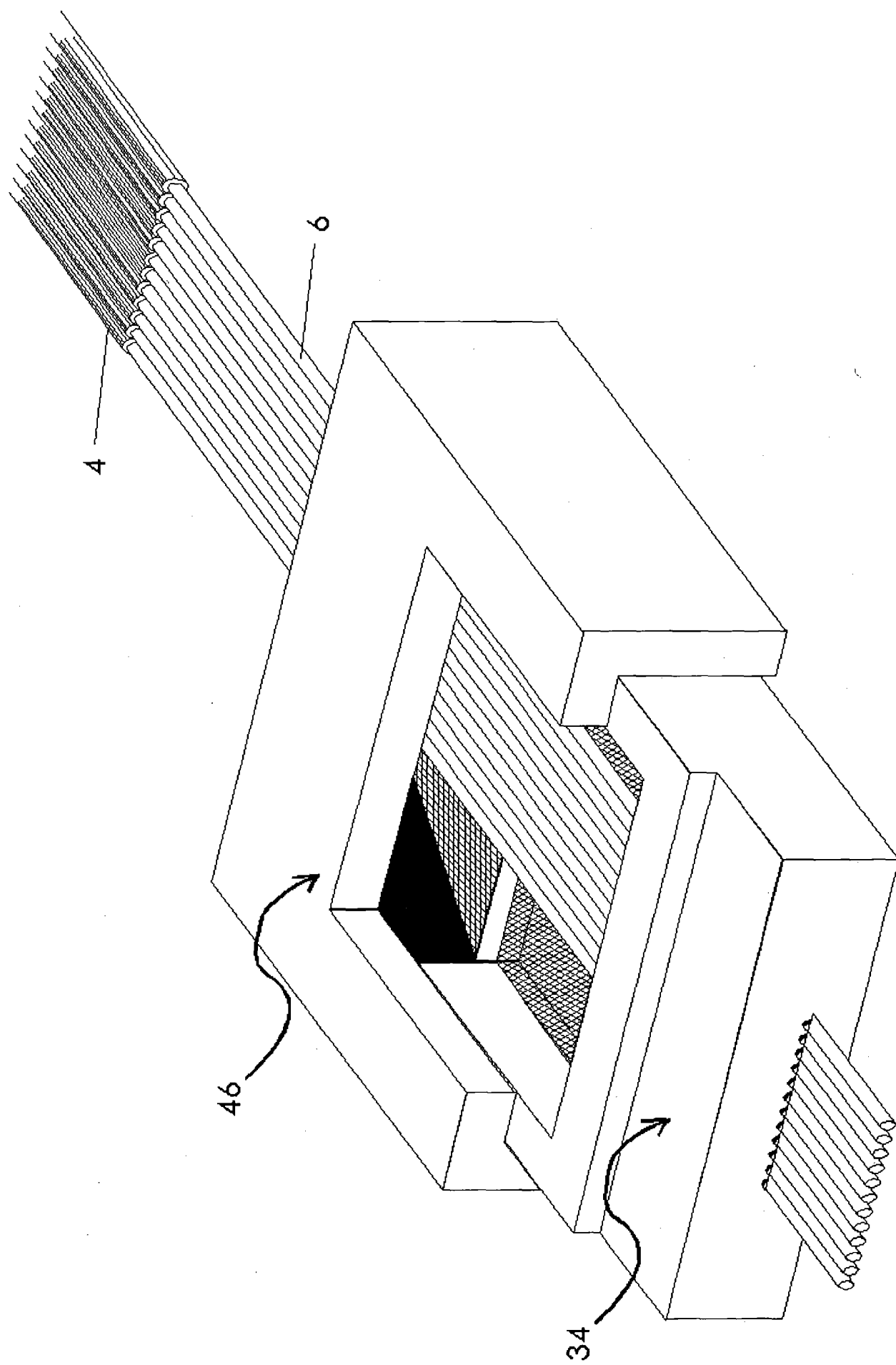


Figure 12 - Both Ferrules and Optical Fibers (Perspective)

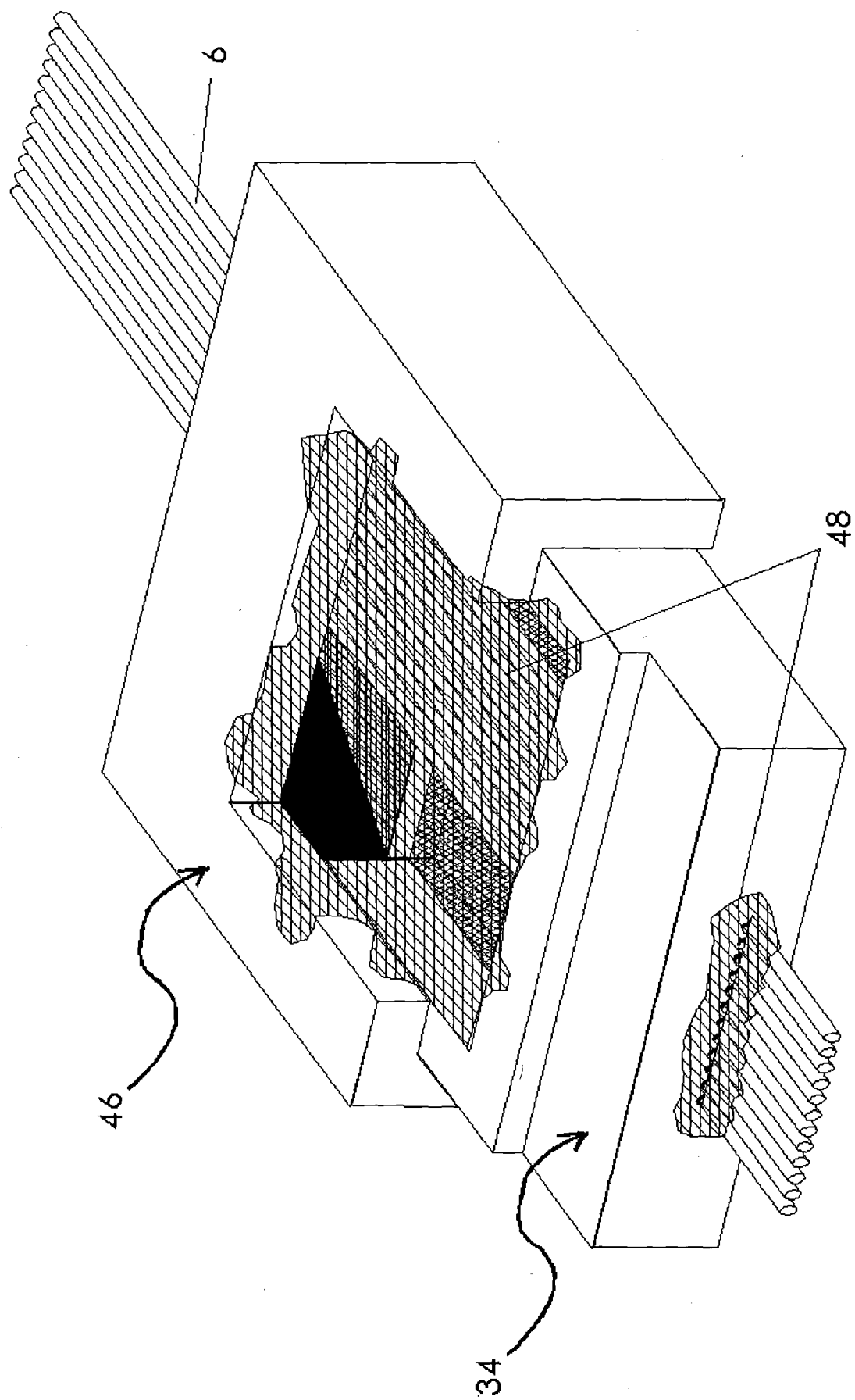


Figure 13 - Cavity and ends filled with Epoxy (Perspective)

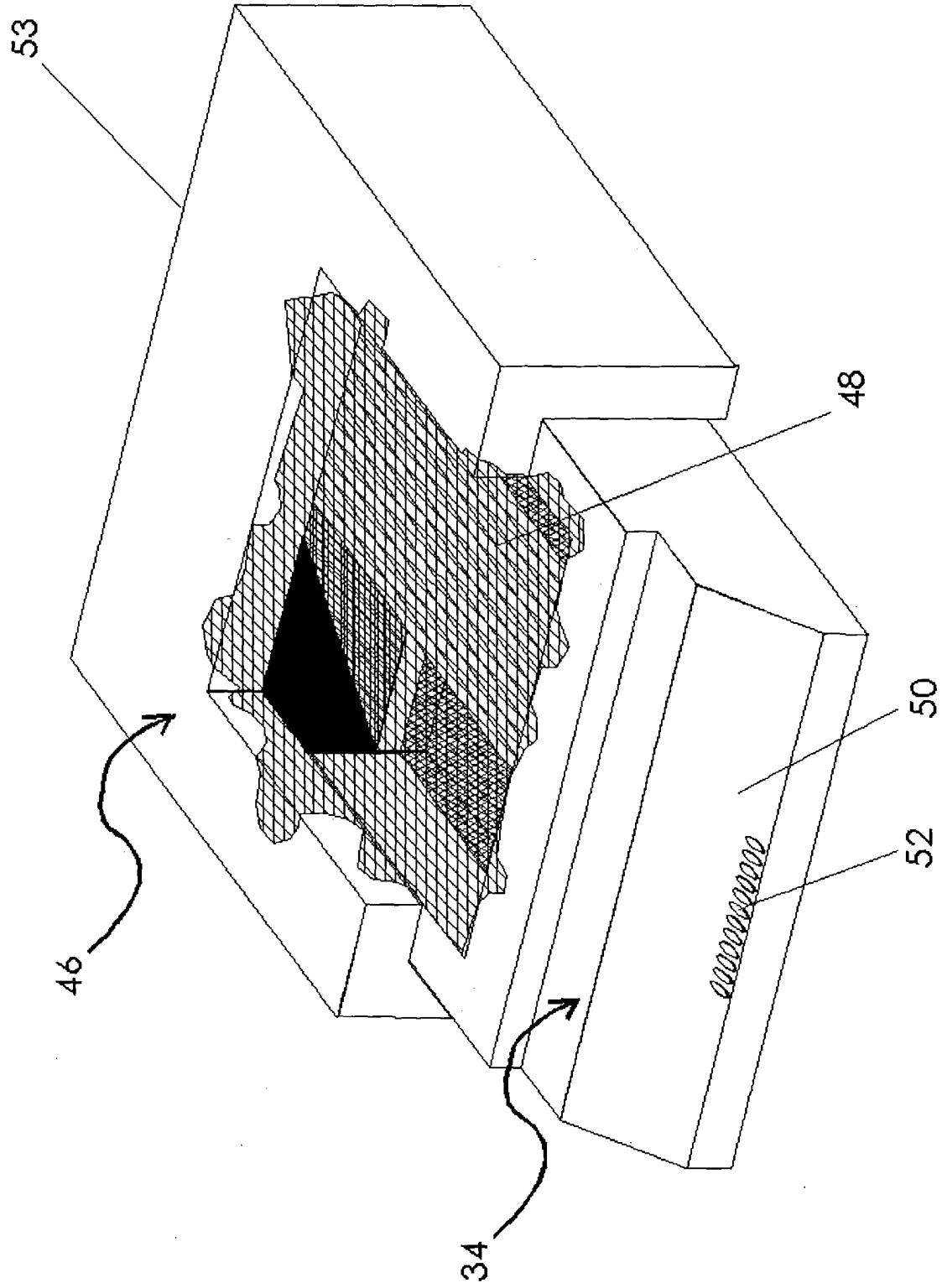


Figure 14 - Bevel (Perspective)

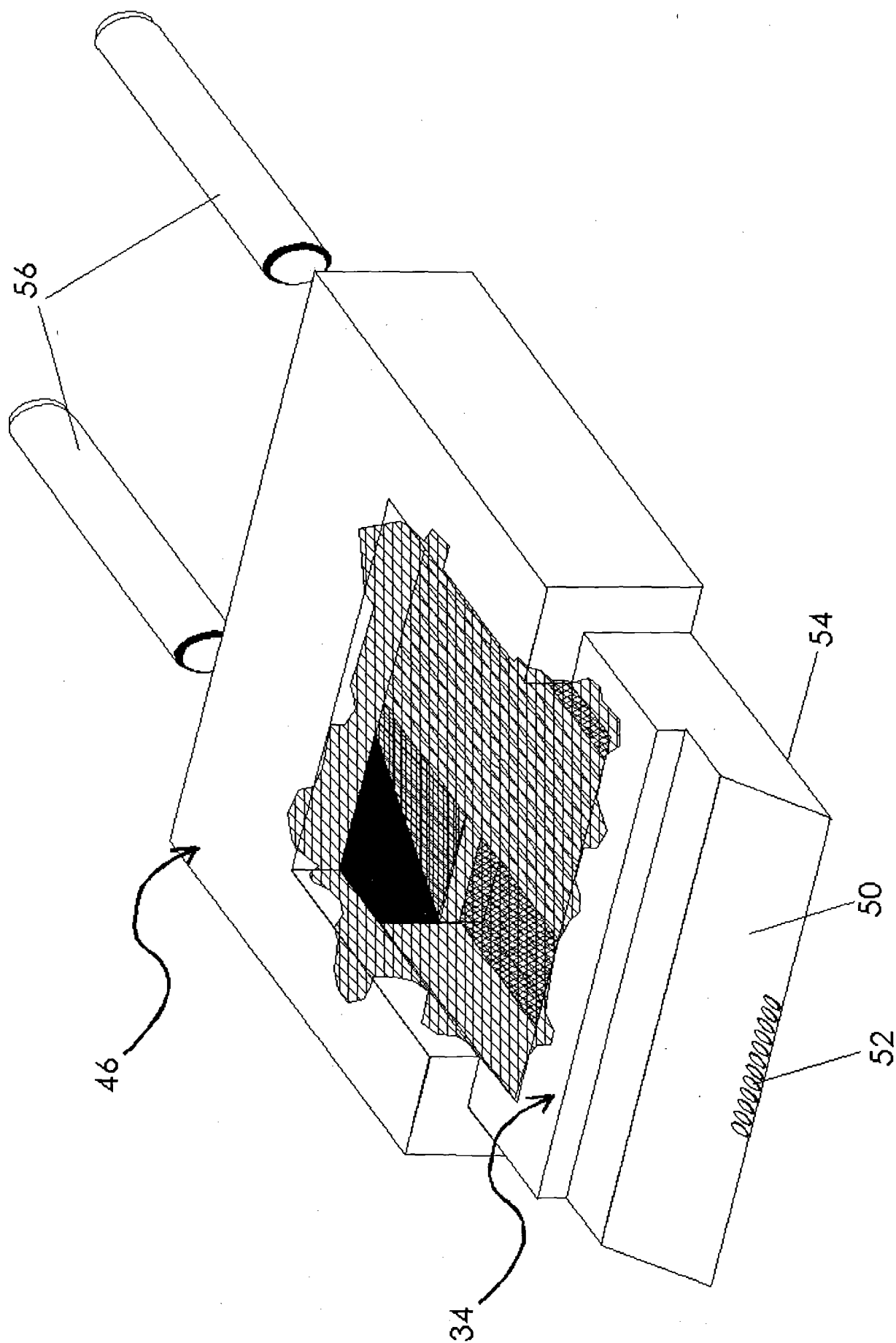


Figure 15 - Flat Polish and Dowel Pins (Perspective)

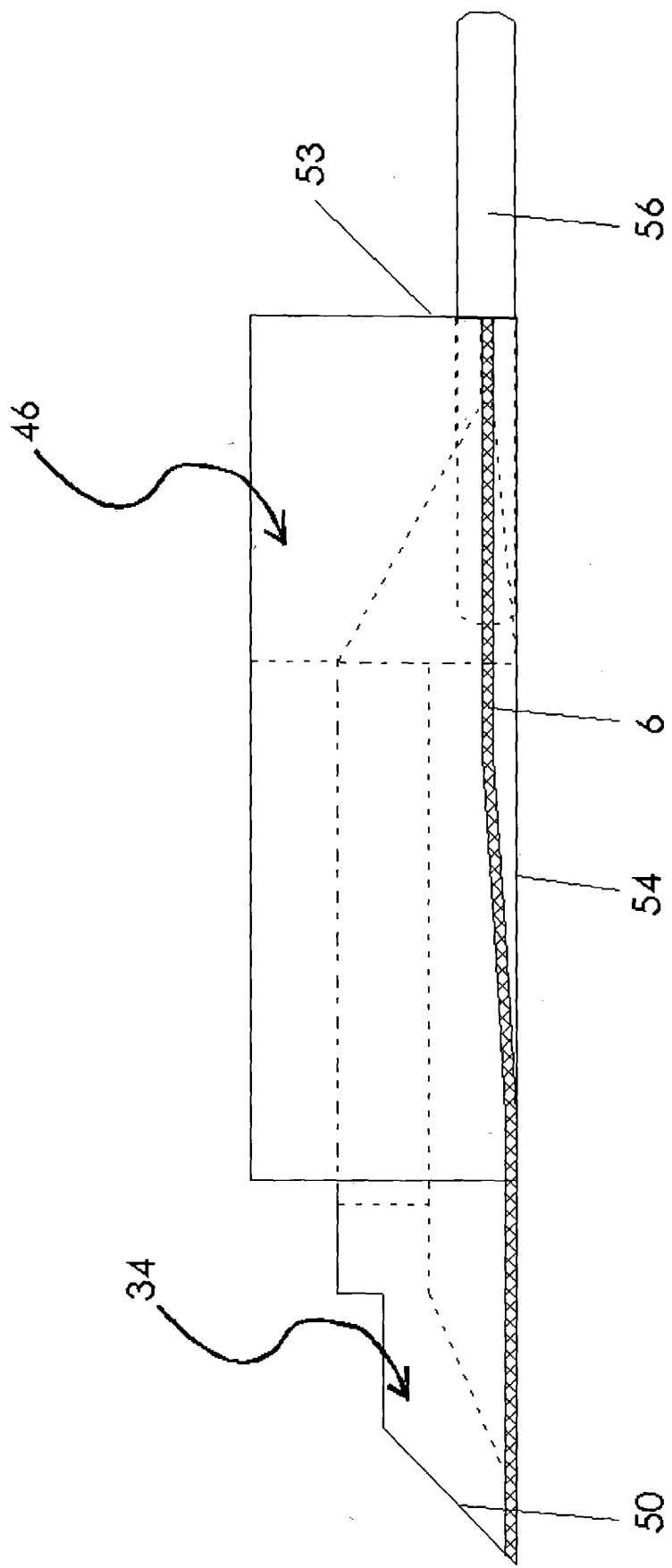


Figure 16 - Complete Ferrule (Side View)

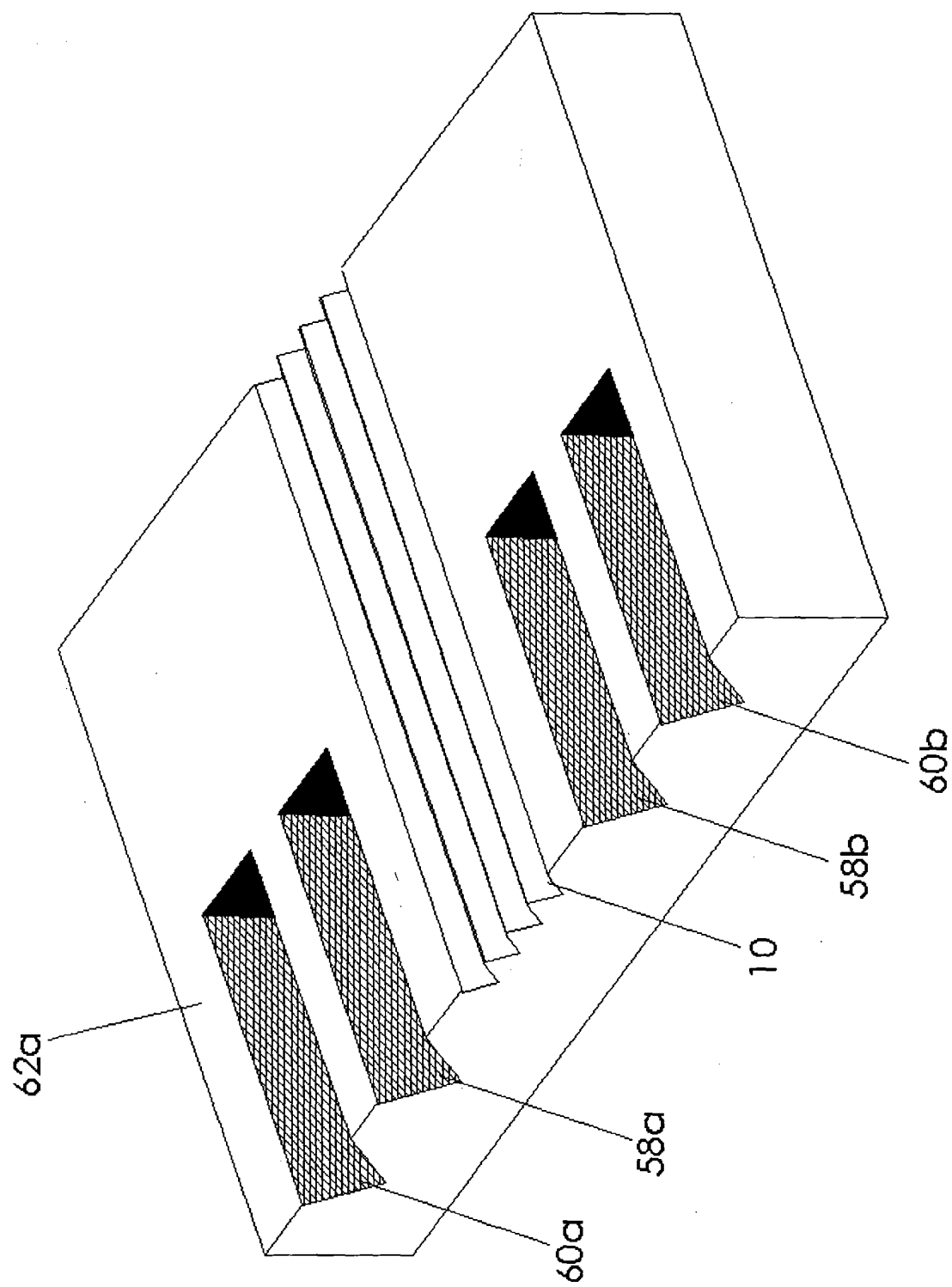


Figure 17 - First Silicon Multi-V-Groove Chip (Perspective)



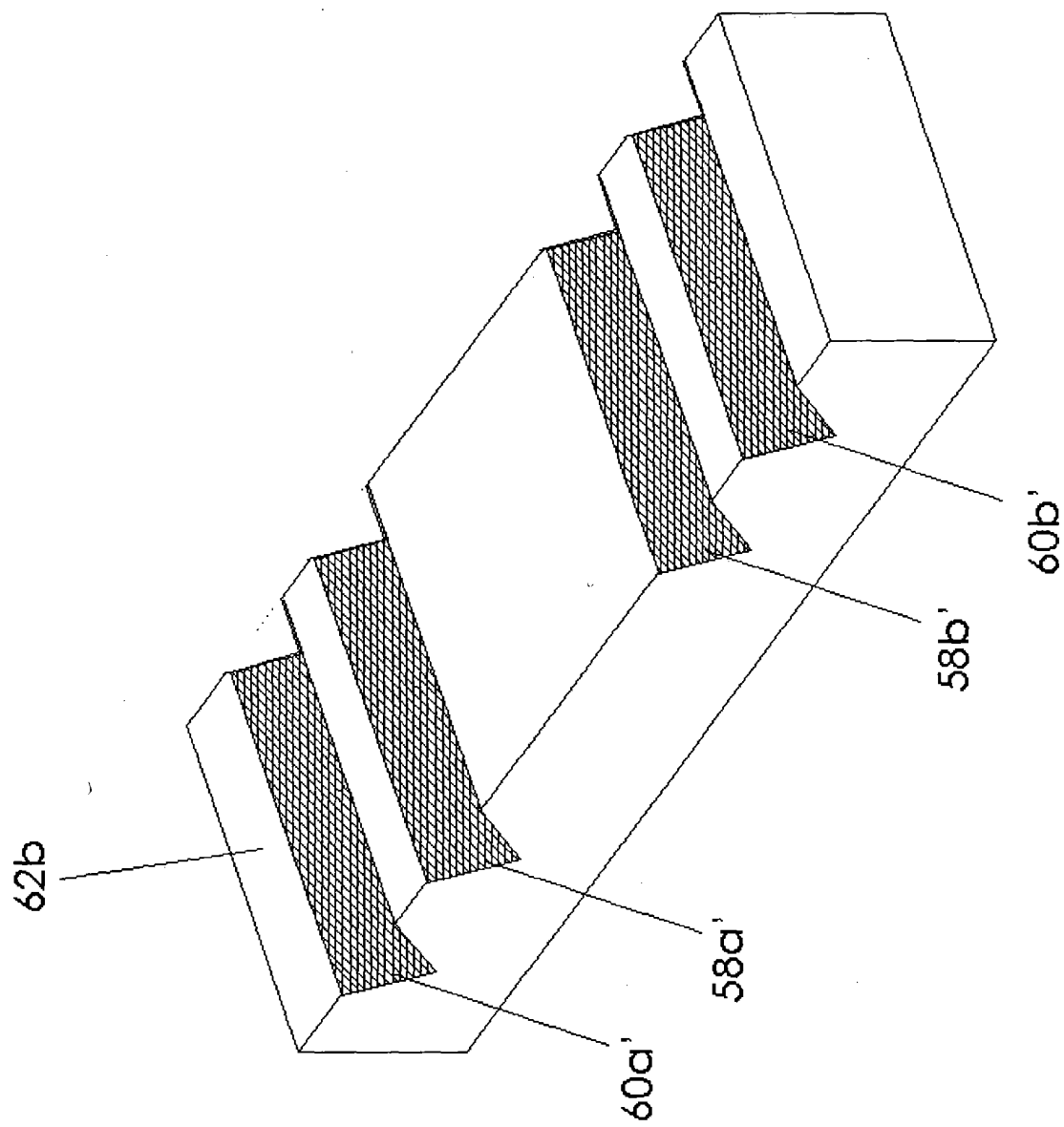


Figure 18 - Second Silicon Multi-V-Groove Chip (Perspective)

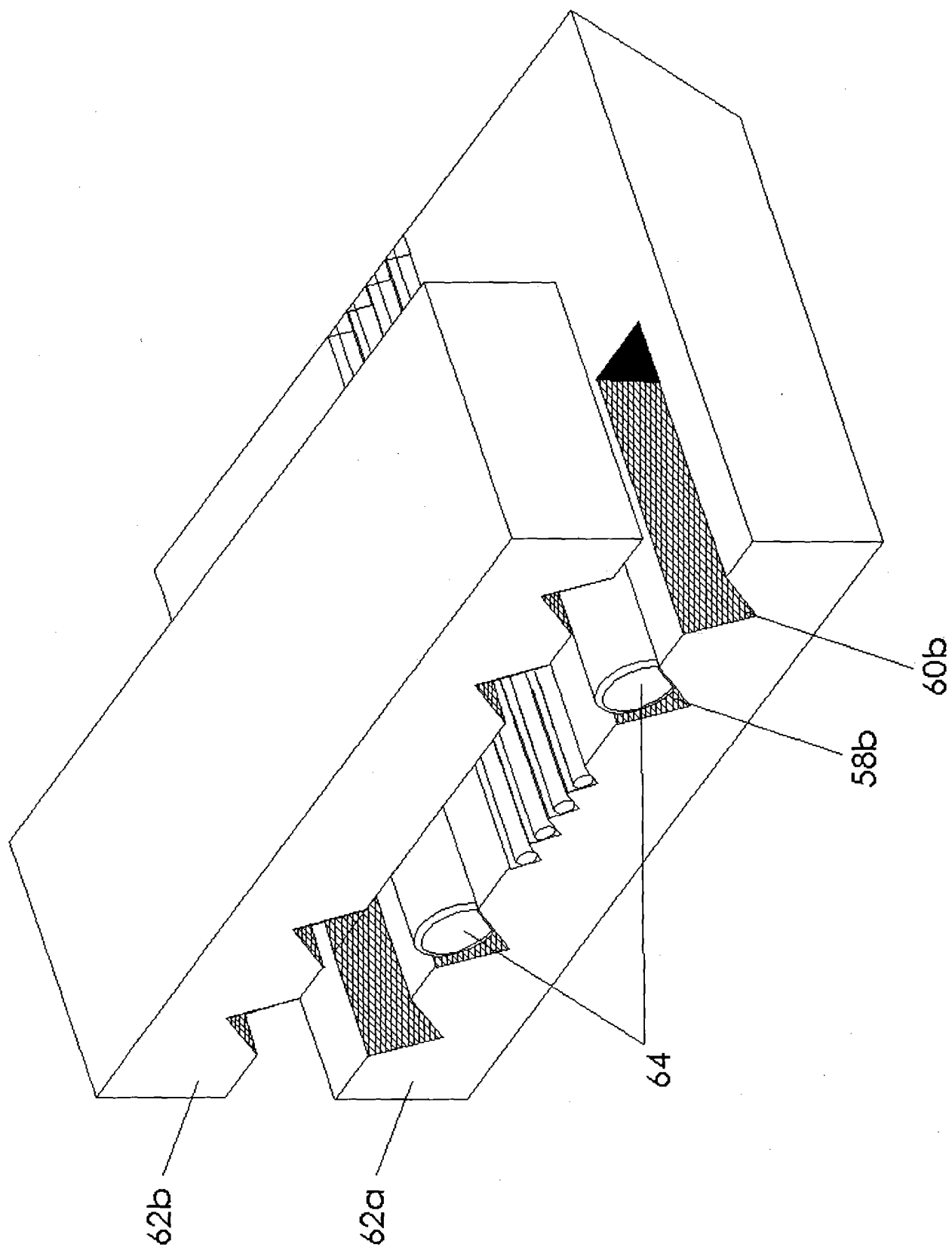


Figure 19 - Intermediate Assembly with Internal Dowel Pins (Perspective)

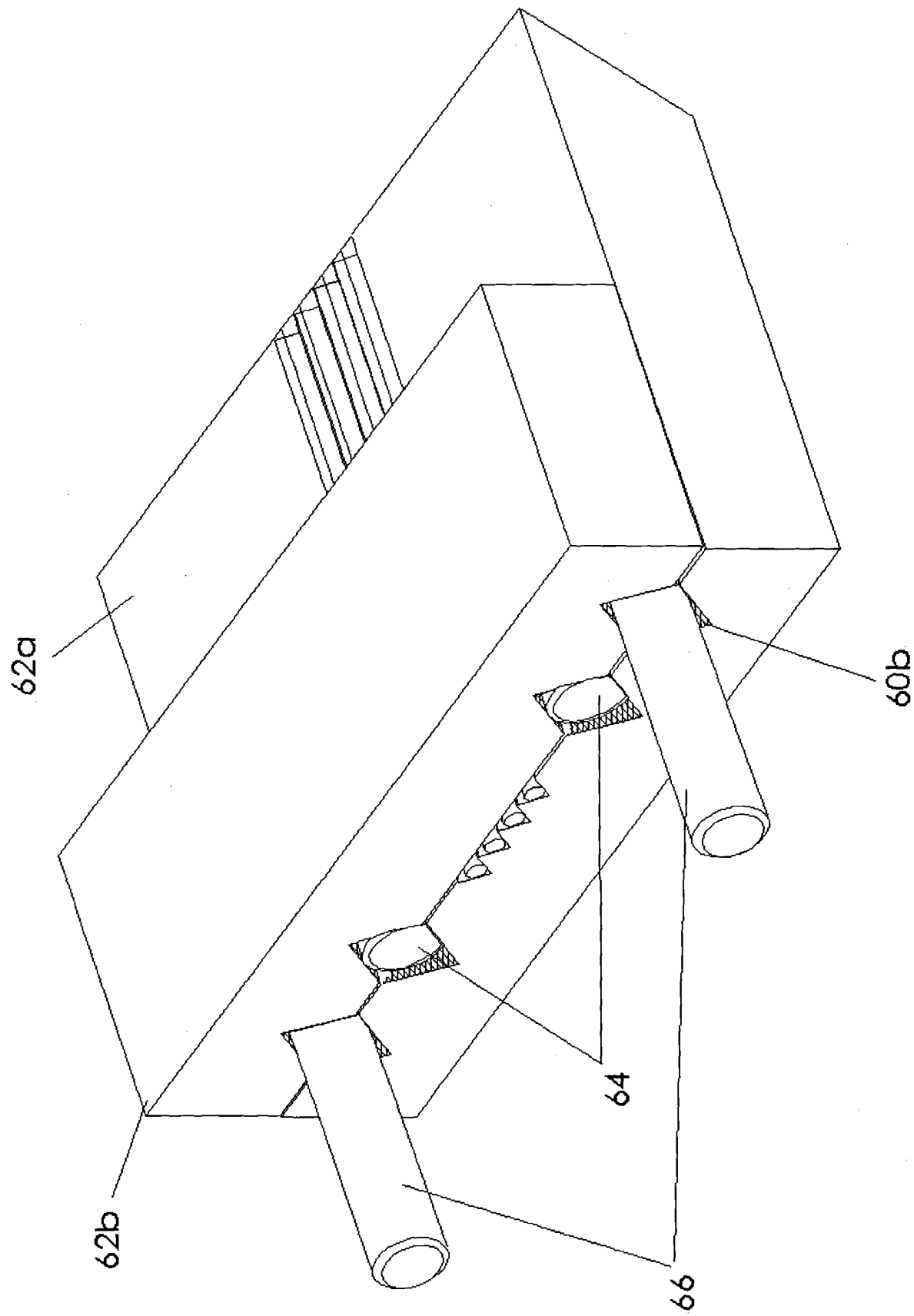


Figure 20 - Final Assembly with External Dowel Pins (Perspective)

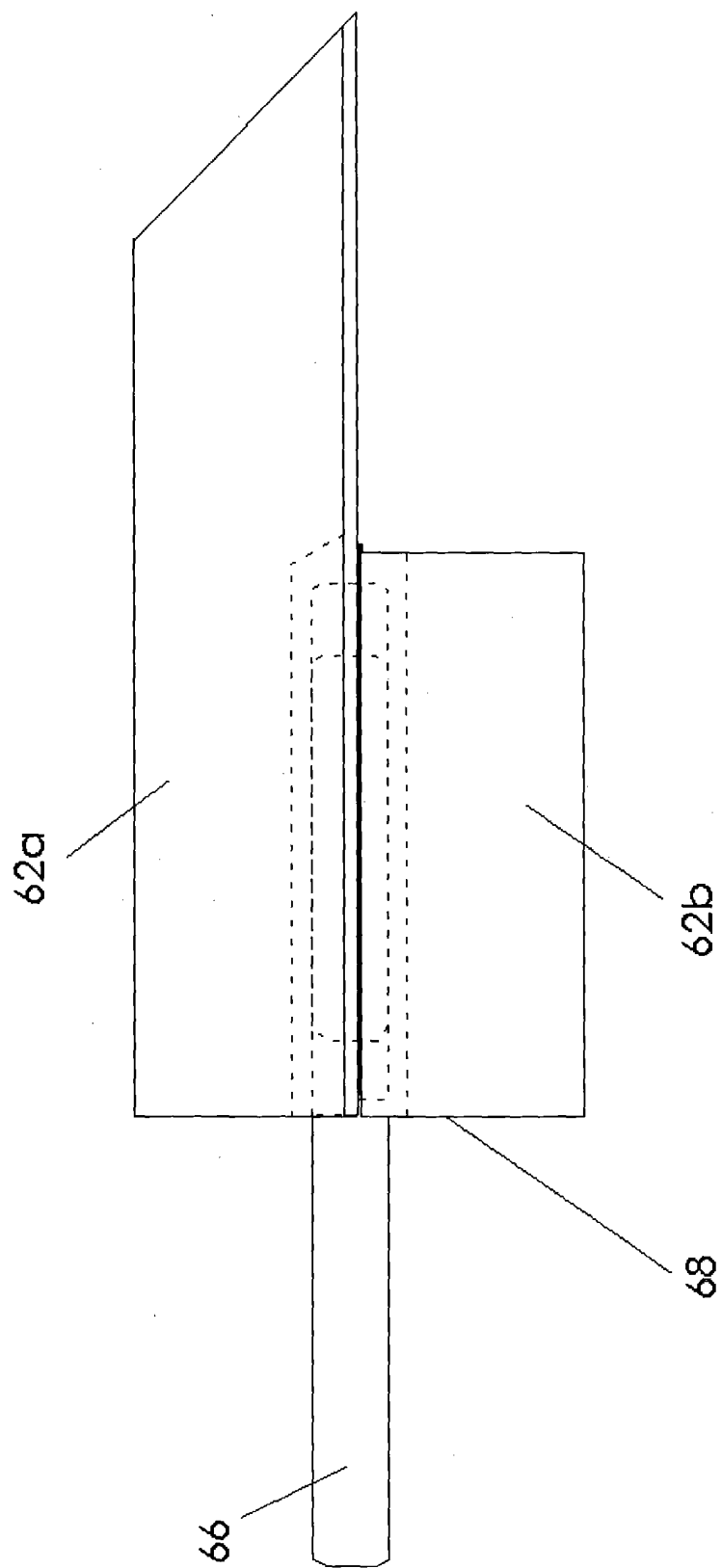


Figure 21 - Final Assembly (to Scale Side View)